



**EULYNX Initiative**

## **EULYNX Domain Knowledge**

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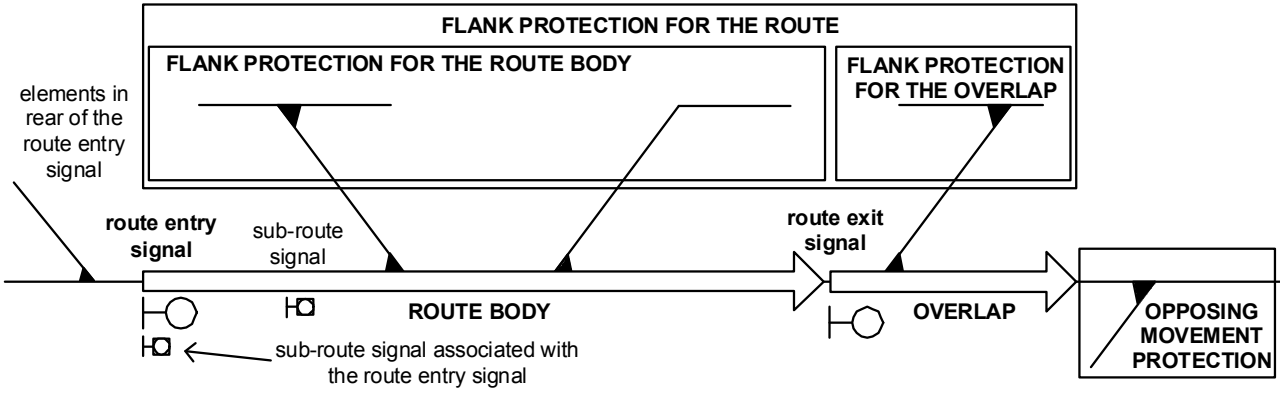
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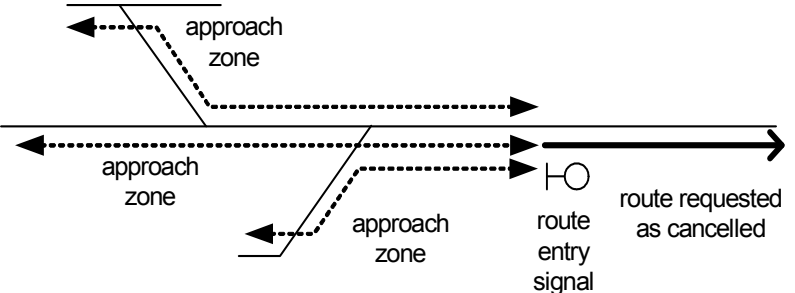
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| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.1   | Head | <b>1 Introduction</b>  |
| Eu.DK.5   | Head | <b>1.1 Release information</b>   |
| Eu.DK.2   | Info | [Eu.Doc.10]<br>EULYNX Domain Knowledge<br>CENELEC Phase: 1-5<br>Version: 1.16 (0.A)<br>Approval date: 15.06.2023   |
| Eu.DK.175 | Info | <b>Version history</b>   |
| Eu.DK.487 | Info | version number: 1.14 (0.A)<br>date: 16.05.2022<br>author: Nico Huurman<br>review: CCB<br>changes: EUGDK-150, EUGDK-154   |
| Eu.DK.488 | Info | version number: 1.15 (0.A)<br>date: 04.04.2023<br>author: Nico Huurman<br>review:<br>changes: EUGDK-159, EUGDK-160, EUGDK-161, EUGDK-163, EUGDK-165, EUGDK-167, EUGDK-168                    |
| Eu.DK.572 | Info | version number: 1.15 (1.A)<br>date: 10.05.2023<br>author: Nico Huurman<br>review: cluster<br>changes: EUGDK-172, EUGDK-173   |
| Eu.DK.573 | Info | version number: 1.16 (0.A)<br>date: 27.06.2023<br>author: Nico Huurman<br>review: CCB<br>changes: EUGDK-177, EUGDK-178, EUGDK-180, EUGDK-181   |
| Eu.DK.3   | Head | <b>1.2 Impressum</b>   |
| Eu.DK.4   | Info | Publisher:<br><b>EULYNX Initiative</b><br><br>A full list of the <b>EULYNX Partners</b> can be found on <a href="http://www.eulynx.eu/index.php/members">www.eulynx.eu/index.php/members</a> |
| Eu.DK.6   | Info | Responsible for this document:<br>EULYNX Project Management Office<br><a href="http://www.eulynx.eu">www.eulynx.eu</a>   |
| Eu.DK.177 | Info | Copyright EULYNX Partners<br>All information included or disclosed in this document is licensed under the European Union Public Licence EUPL, Version 1.2 or later.                          |
| Eu.DK.7   | Head | <b>1.3 Purpose</b>   |
| Eu.DK.8   | Info | The purpose of this document is the provision of the domain knowledge relevant for textual and modelled specifications of the EULYNX system.   |
| Eu.DK.10  | Head | <b>2 Routes</b>  |
| Eu.DK.190 | Head | <b>2.1 General definitions</b>   |
| Eu.DK.191 | Info | 'Locking' is the supervision in an interlocking system that prevents the movement of elements or their use in another route or area.   |

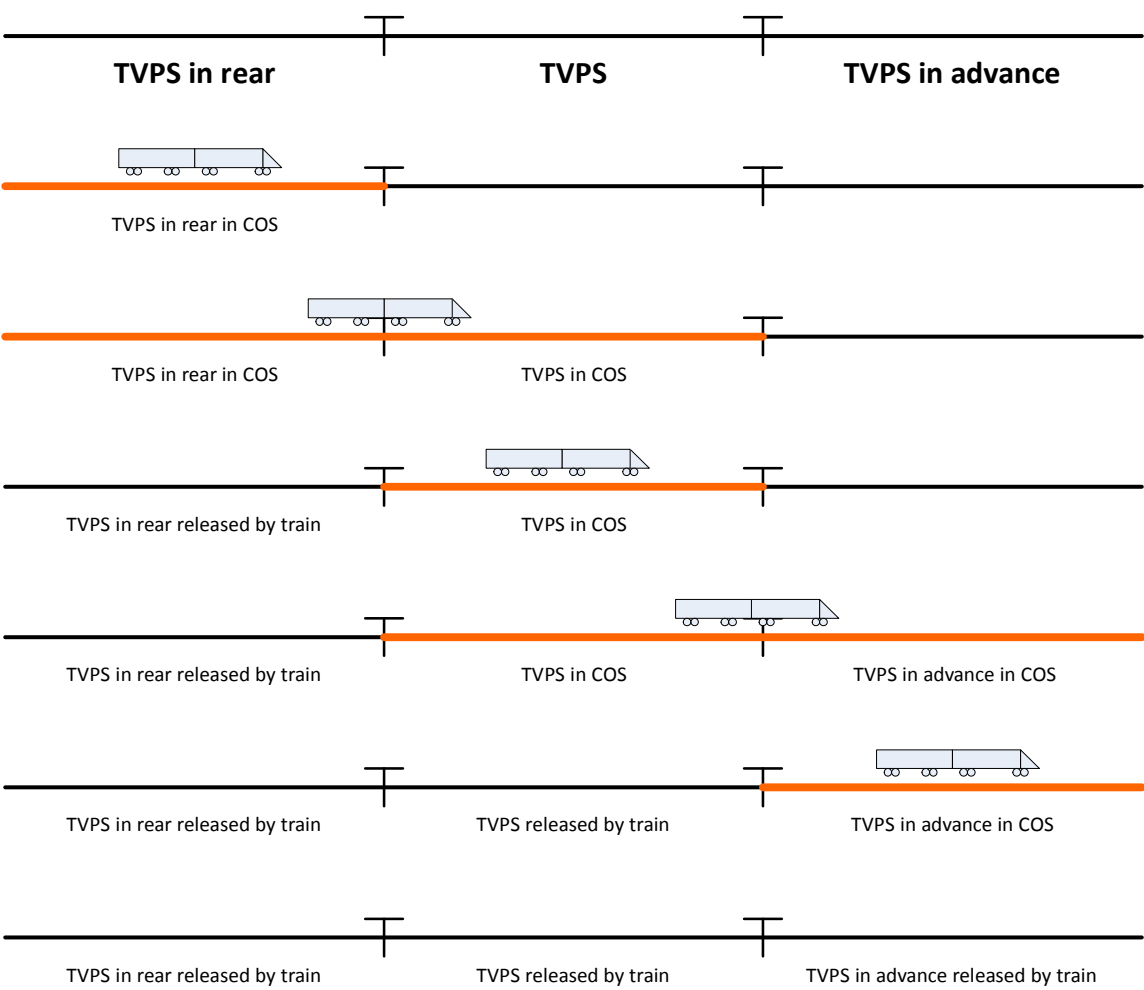
| ID        | Type | Domain knowledge   |
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| Eu.DK.192 | Info | 'Monitoring' is an interlocking system process ensuring that the conditions in a route for the display of a movement authority are continuously met.   |
| Eu.DK.193 | Info | 'Releasing' is the process of unlocking elements from a route.   |
| Eu.DK.194 | Info | 'Cancellation' is the revocation or annulment of a route or part of a route following a request from the signaller.  |
| Eu.DK.195 | Info | 'Blocking' is the process of immobilising equipment or provision of protection against train movement into blocked elements or areas.  |
| Eu.DK.11  | Head | <b>2.2 Route Definition</b>  |
| Eu.DK.12  | Info | <p>A route is a predetermined path for a traffic movement.<br/>                     It may consist of the following:</p> <ul style="list-style-type: none"> <li>• the route body</li> <li>• flank protection for the route body</li> <li>• the overlap</li> <li>• flank protection for the overlap</li> <li>• the route elements in rear of the route entry signal</li> </ul>  |
| Eu.DK.181 | Info | Overlap is a defined section of track in advance of the route exit signal, which must be kept clear to avoid the risk of collision should a train inadvertently run past the signal.   |
| Eu.DK.15  | Info | The following diagram displays the terminology for the route and its possible elements.  |
| Eu.DK.16  | Info |  <p>The diagram illustrates the components of a route and its protection zones. From left to right, it shows:                     <ul style="list-style-type: none"> <li>'elements in rear of the route entry signal' (indicated by a line pointing to the left).</li> <li>'route entry signal' (a signal post with a circle).</li> <li>'sub-route signal' (a signal post with a square).</li> <li>'ROUTE BODY' (the main section of the route).</li> <li>'sub-route signal associated with the route entry signal' (a signal post with a square).</li> <li>'route exit signal' (a signal post with a circle).</li> <li>'OVERLAP' (a section of track beyond the route exit signal).</li> <li>'OPPOSING MOVEMENT PROTECTION' (a box representing protection for opposing traffic).</li> </ul>                     Protection zones are shown as boxes above the track:                     <ul style="list-style-type: none"> <li>'FLANK PROTECTION FOR THE ROUTE' (a large box encompassing the route body and overlap).</li> <li>'FLANK PROTECTION FOR THE ROUTE BODY' (a box covering the route body).</li> <li>'FLANK PROTECTION FOR THE OVERLAP' (a box covering the overlap).</li> </ul> </p> |
| Eu.DK.17  | Info | <p>The elements that are considered as part of the route are:</p> <ul style="list-style-type: none"> <li>• route entry signal</li> <li>• route exit signal</li> <li>• sub-route signal (can be a main or a shunting signal)</li> <li>• TVP sections in the route body</li> <li>• TVP sections in the overlap</li> <li>• moveable elements in the route body</li> <li>• moveable elements in the overlap</li> <li>• moveable elements for flank protection</li> <li>• moveable elements in rear of the route entry signal, such as middle points</li> <li>• TVP sections in rear of the route entry signal</li> <li>• lockable devices</li> </ul>   |
| Eu.DK.18  | Info | <p>The elements that are not considered as part of the route, but are driven and/or supervised by the route, are:</p> <ul style="list-style-type: none"> <li>• signals providing flank protection to the route body</li> <li>• signals providing flank protection to the overlap</li> <li>• opposing signals in the route body</li> <li>• opposing signals to the route body</li> <li>• TVP sections in the flank zone of the route body</li> <li>• TVP sections in the flank zone of the overlap</li> <li>• detection devices</li> <li>• level crossings</li> <li>• line blocks</li> </ul>  |

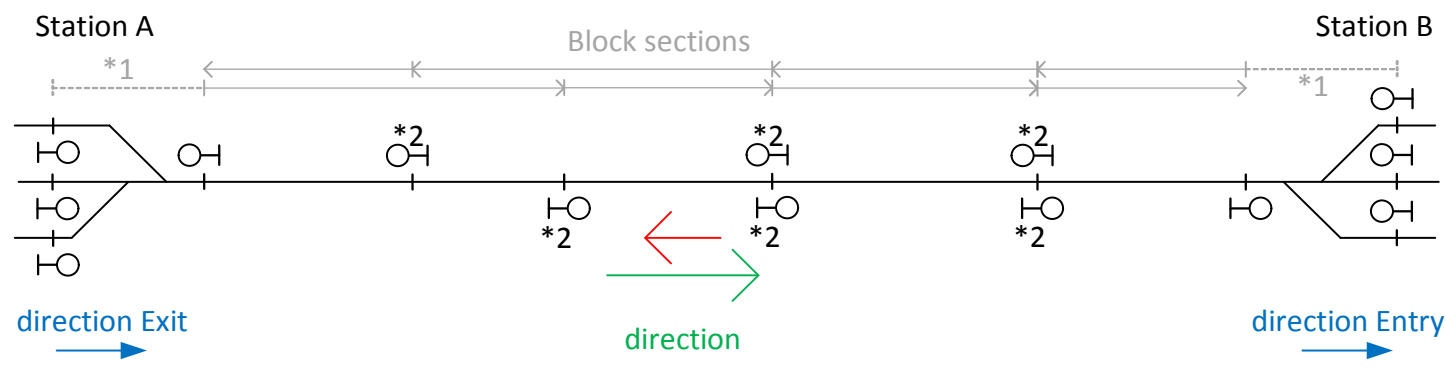
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| Eu.DK.19  | Info | Virtual route exit signals may be any of the following: <ul style="list-style-type: none"> <li>• <i>dark territory</i></li> <li>• <i>end of track</i></li> <li>• <i>open line</i></li> <li>• <i>stop sign</i></li> </ul>   |
| Eu.DK.20  | Info | The following diagram displays the monitored signals that are not part of the route.   |
| Eu.DK.21  | Info |  |
| Eu.DK.22  | Info | The following diagram displays the use of TVP sections by a route.   |
| Eu.DK.23  | Info |  |
| Eu.DK.24  | Info | The following diagram displays the use of the destination track and its TVP sections.  |
| Eu.DK.25  | Info |  |
| Eu.DK.26  | Info | The destination track may also contain a middle point. A middle point is a point locked by a route, although located in rear of the route body.  |
| Eu.DK.27  | Info | The destination track may be a dead-end track.   |
| Eu.DK.28  | Head | <b>2.3 Route Life Cycle</b>  |
| Eu.DK.182 | Info | Route setting is the interlocking system process of allocating, positioning and locking moveable track elements into a route.  |
| Eu.DK.29  | Info | A route is considered as: <ul style="list-style-type: none"> <li>• <i>'requested'</i> if a request for a route is received by the interlocking system</li> <li>• <i>'rejected'</i> in a situation when the conditions for setting a route are not fulfilled and the route is not set</li> <li>• <i>'prepared'</i> if the route has been requested, but not all objects of the route are available at the time of the request (route preparation ensures operational optimisation by reduction of switching time of route elements)</li> <li>• <i>'initiated'</i> if the route request was accepted, until the moment the route becomes locked</li> <li>• <i>'locked'</i> if all the route elements required to be locked are locked</li> </ul> |

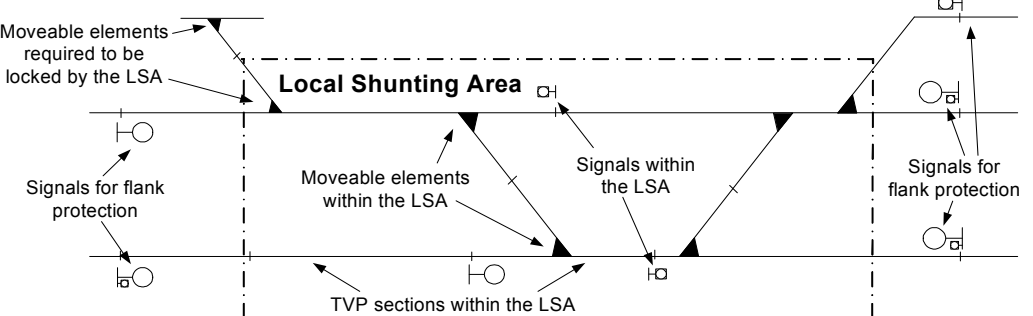
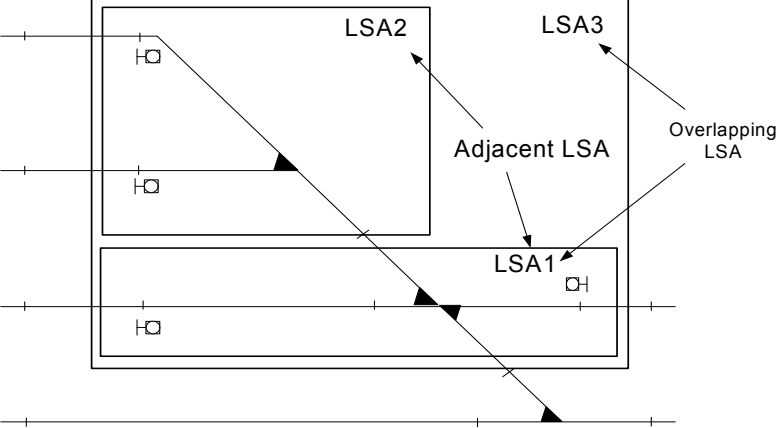
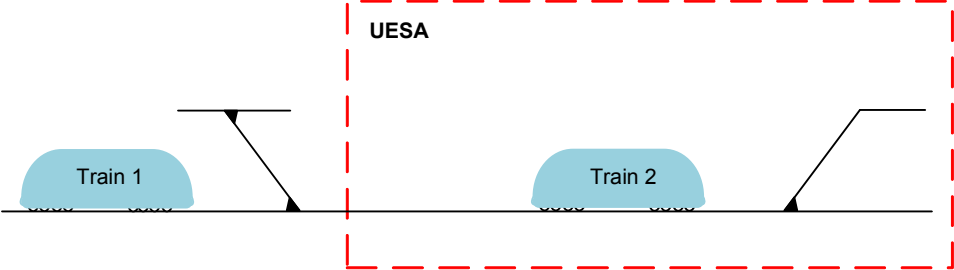
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| Eu.DK.30  | Info | An element is considered as: <ul style="list-style-type: none"> <li>• 'used' if the element is part of a route that is 'initiated' or 'locked'</li> <li>• 'locked' if a route requires the element to be locked and the element is locked</li> </ul>                               |
| Eu.DK.31  | Info | An element is considered as a 'route element' only while it is 'used' by a route. For example, a signal is a route entry signal only if a route exists that uses that signal as a route entry.   |
| Eu.DK.32  | Info | An individual route is intended to be traversed by one train only.   |
| Eu.DK.33  | Info | The use and locking of a route element is particular to an individual route. If the same route is set again, the route element is used and locked in the new route.  |
| Eu.DK.461 | Info | If no contradicting conditions are present, an element can be used and locked in more than one route. For example a point can be in the route body of one route and act as flank protection for another route, if both routes require the point to be locked in the same position. |
| Eu.DK.34  | Info | A route element that is used and locked in multiple routes shall have the locking applied independently by the different individual routes.  |
| Eu.DK.183 | Info | A 'residual route' remains if part of a route is not released after the passage of a train (e.g. incorrect train operated route release, stopped train, turnback movement).  |
| Eu.DK.35  | Head | <b>2.4 Approach Zone Definition</b>  |
| Eu.DK.36  | Info | The approach zone is used to detect a vehicle on a valid approach towards the route entry signal. It provides the conditions governing a delayed or immediate route release after a cancellation request.  |
| Eu.DK.37  | Info | The following diagram displays the use of multiple approach zones for a route.   |
| Eu.DK.38  | Info |   |
| Eu.DK.39  | Head | <b>2.5 Route Release</b>   |
| Eu.DK.40  | Info | The following diagrams display the elements used to determine the correct 'occupancy sequence' for train operated route release.   |

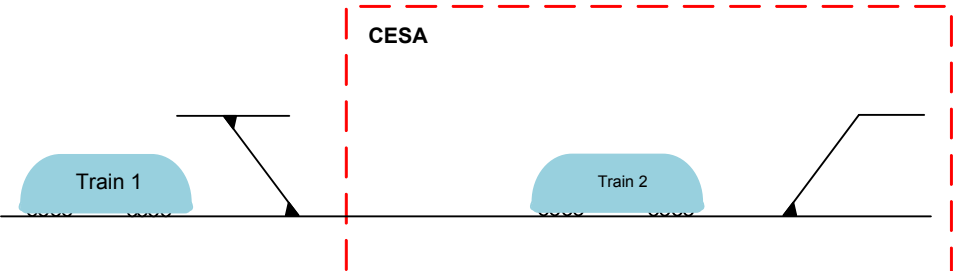
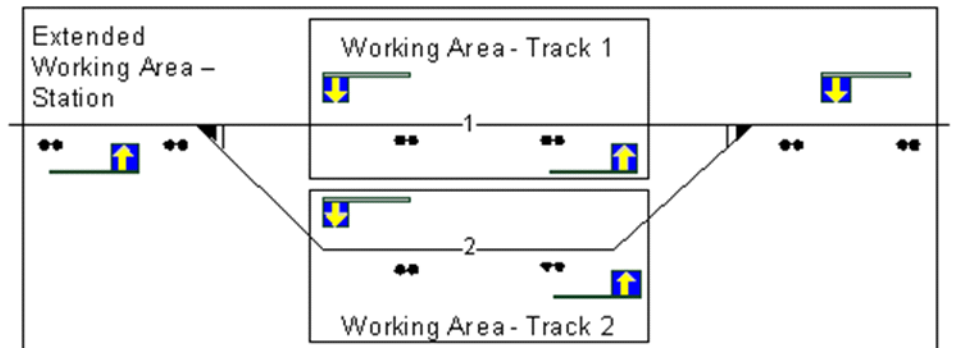


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| Eu.DK.372 | Info | <p><b>Correct occupancy sequence (COS) - variant 1</b></p> <p>The diagram illustrates the correct occupancy sequence (COS) for a train passing through three TVPS (TVPS in rear, TVPS, TVPS in advance) in six stages. The stages are as follows:</p> <ul style="list-style-type: none"> <li><b>Stage 1:</b> TVPS in rear, TVPS, TVPS in advance.</li> <li><b>Stage 2:</b> TVPS in rear in COS.</li> <li><b>Stage 3:</b> TVPS in rear in COS, TVPS in COS.</li> <li><b>Stage 4:</b> TVPS in rear in COS, TVPS in COS, TVPS in advance in COS.</li> <li><b>Stage 5:</b> TVPS in rear released by train, TVPS in COS, TVPS in advance in COS.</li> <li><b>Stage 6:</b> TVPS in rear released by train, TVPS released by train, TVPS in advance in COS.</li> <li><b>Stage 7:</b> TVPS in rear released by train, TVPS released by train, TVPS in advance released by train.</li> </ul> |

| ID        | Type | Domain knowledge   |
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| Eu.DK.373 | Info | <p><b>Correct occupancy sequence (COS) - variant 2</b></p>  <p>The diagram illustrates the sequence of TVPS (Train Vehicle Positioning System) occupancy and release for a train moving from left to right across three TVPS locations: TVPS in rear, TVPS, and TVPS in advance. The track is represented by a horizontal line with three vertical tick marks indicating the TVPS locations. The train is shown as a blue rectangle with wheels.</p> <ul style="list-style-type: none"> <li><b>Stage 1:</b> TVPS in rear is occupied by the train. Labels: TVPS in rear, TVPS, TVPS in advance.</li> <li><b>Stage 2:</b> TVPS in rear is released by the train, and TVPS in rear in COS is occupied. Label: TVPS in rear in COS.</li> <li><b>Stage 3:</b> TVPS in rear is released by the train, TVPS in rear in COS is occupied, and TVPS in COS is occupied. Labels: TVPS in rear in COS, TVPS in COS.</li> <li><b>Stage 4:</b> TVPS in rear is released by the train, TVPS in COS is occupied, and TVPS in advance is occupied. Labels: TVPS in rear released by train, TVPS in COS.</li> <li><b>Stage 5:</b> TVPS in rear is released by the train, TVPS in COS is occupied, and TVPS in advance in COS is occupied. Labels: TVPS in rear released by train, TVPS in COS, TVPS in advance in COS.</li> <li><b>Stage 6:</b> TVPS in rear is released by the train, TVPS in COS is released by the train, and TVPS in advance in COS is occupied. Labels: TVPS in rear released by train, TVPS released by train, TVPS in advance in COS.</li> <li><b>Stage 7:</b> TVPS in rear is released by the train, TVPS in COS is released by the train, and TVPS in advance is released by the train. Labels: TVPS in rear released by train, TVPS released by train, TVPS in advance released by train.</li> </ul> |
| Eu.DK.432 | Info | <p>For specific train types (e.g. a special transport which does not duly occupy the track sections), the train operated route release may be inhibited. This function can be used as a mitigating measure against a premature release of a train route which can cause a too early locking of a new conflicting train route.</p>  |
| Eu.DK.196 | Head | <p><b>3 Line block</b></p>   |
| Eu.DK.197 | Info | <p>A line block is a section of the railway between two stations controlled by a line block system.</p>  |
| Eu.DK.198 | Info | <p>In an automatic line block system, certain fixed signals for block sections are operated automatically by the passage of trains, depending on the state of the line block track.</p>  |
| Eu.DK.207 | Info | <p>In a route based line block system, the fixed signals for the block sections are operated by an interlocking, based on route setting. Route setting can be performed automatically, manually or by an automatic route setting system (ARS).</p>   |
| Eu.DK.199 | Info | <p>A block section is a section of track between two successive block signals, which ensure the protection of trains in the section.</p>   |
| Eu.DK.208 | Info | <p>If the railway section controlled by a line block system consists of more than one track, the line block of each track functions independently.</p>   |
| Eu.DK.209 | Head | <p><b>3.1 Direction</b></p>  |
| Eu.DK.200 | Info | <p>A line block track has a determined direction of movement of trains on the track, which is synchronised between the interlocking systems of the two adjacent stations, so that rail vehicle movements can be safely performed in that direction.</p>  |
| Eu.DK.210 | Info | <p>The direction of each track of a railway section controlled by a line block system is set independently.</p>  |
| Eu.DK.211 | Info | <p>A determined direction corresponds to one of the two adjacent stations having the direction set to 'Exit' and the other having the direction set to 'Entry' for the respective track.</p>   |
| Eu.DK.212 | Info | <p>A station adjacent to a line block can have the direction set to 'no direction'. This state is used upon start-up of the line block system when the last known direction information is not available.</p>  |

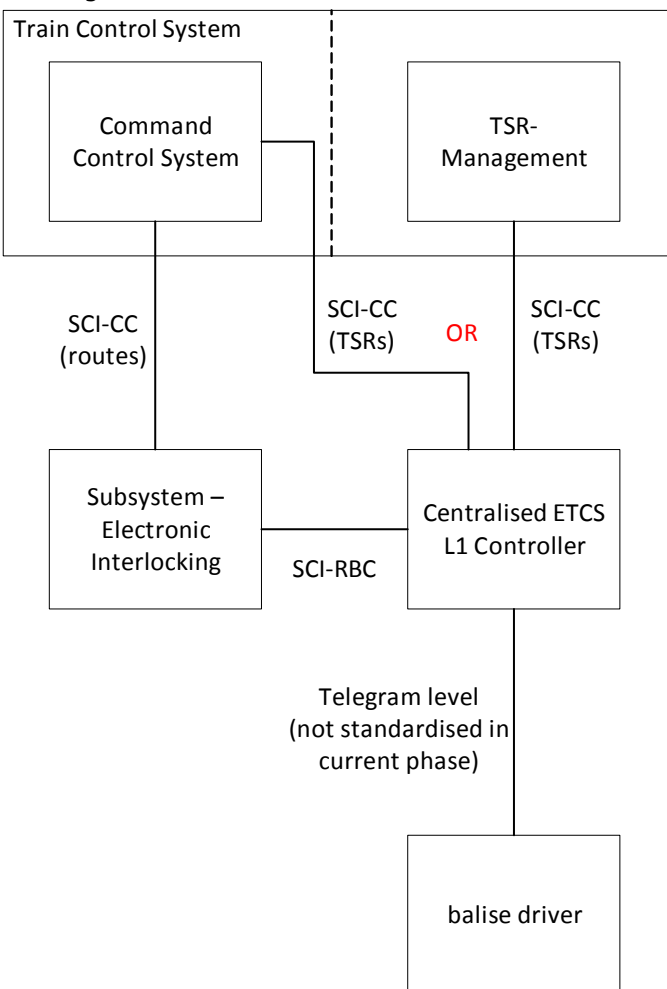
| ID        | Type | Domain knowledge   |
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| Eu.DK.378 | Info | A station adjacent to a line block can have the direction set to 'idle'. This state is used for a specific line block configuration in which the direction is controlled by route setting and train movement.  |
| Eu.DK.217 | Info | <p>The diagram below shows the main definitions regarding a line block system and direction.</p>  <div style="border: 1px solid black; padding: 5px; margin-top: 10px;"> <p>*1: Depending on national rules regarding stations and open line, the dotted line can be considered as being/ not being part of the first block section</p> <p>*2: In an automatic line block system, these block signals can function as signals operated automatically by the passage of trains (see also [Eu.DK.198])</p> </div>  |
| Eu.DK.215 | Head | <b>3.2 Line block with level crossing</b>  |
| Eu.DK.216 | Info | If a track section with an automatic line block system contains a level crossing, additional functionality may be necessary to combine the automatic functioning of line block (direction and/or line block signals) with the functioning of the level crossing.   |
| Eu.DK.46  | Head | <b>4 Areas</b>   |
| Eu.DK.95  | Head | <b>4.1 General</b>   |
| Eu.DK.96  | Info | Objects in a defined area may be grouped together into areas in order to perform tasks together.   |
| Eu.DK.97  | Info | All areas are implemented during the engineering process, and require an operational identifier assigned to each of them. These identifiers are used by the interlocking system, Radio Block Centre and Traffic Control System for communication about activation and deactivation of the different areas.   |
| Eu.DK.47  | Head | <b>4.2 Local Shunting Area</b>   |
| Eu.DK.48  | Head | <b>4.2.1 Local Shunting Area Definition</b>  |
| Eu.DK.49  | Info | <p>A local shunting area consists of the following elements:</p> <ul style="list-style-type: none"> <li>• the TVP sections within the local shunting area</li> <li>• the signals within the local shunting area</li> <li>• the moveable elements within the local shunting area</li> <li>• the lockable devices within the local shunting area</li> <li>• the signals required to display a 'stop' aspect for flank protection</li> <li>• the moveable elements required to be 'locked' before enabling the local shunting area</li> <li>• the lockable devices required to be 'locked' before enabling the local shunting area</li> </ul> |
| Eu.DK.51  | Info | The following diagram displays the terminology for the local shunting area:  |

| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.52  | Info |   |
| Eu.DK.53  | Info | The LECP (Local Element Control Panel) is an external panel located trackside which allows the shunter to manipulate with an enabled local shunting area.   |
| Eu.DK.54  | Head | <b>4.2.2 Adjacent or Overlapping Local Shunting Area</b>  |
| Eu.DK.55  | Info | Adjacent or overlapping local shunting areas may be enabled, simultaneously or one after the other.   |
| Eu.DK.56  | Info | All released elements in the resulting local shunting area shall have flank protection when the resulting local shunting area is enabled.   |
| Eu.DK.57  | Info |    |
| Eu.DK.58  | Head | <b>4.2.3 Local Shunting Area Life Cycle</b>   |
| Eu.DK.59  | Info | <p>A local shunting area is considered as:</p> <ul style="list-style-type: none"> <li>• 'initiated' if the request is not rejected, until the local shunting area becomes enabled in the activation process or disabled in the deactivation process</li> <li>• 'enabled' if the initiation of the local shunting area is completed</li> <li>• 'disabled' if the withdrawal of an initiated or enabled local shunting area is completed</li> </ul> |
| Eu.DK.98  | Head | <b>4.3 Emergency Stop Area</b>  |
| Eu.DK.99  | Info | Emergency stop areas are used when unwanted situations occur. These areas are divided into two categories, unconditional emergency stop areas (UESA) and conditional emergency stop areas (CESA).   |
| Eu.DK.100 | Head | <b>4.3.1 Unconditional Emergency Stop Area</b>  |
| Eu.DK.102 | Info | UESA is also referred to as Emergency stop area. When the area is activated, trains approaching the area (Train 1) will receive a conditional emergency stop. Trains inside the area (Train 2) will receive an unconditional emergency stop. The following diagram displays the UESA scenario:  |
| Eu.DK.154 | Info | <p>UESA scenario</p>    |

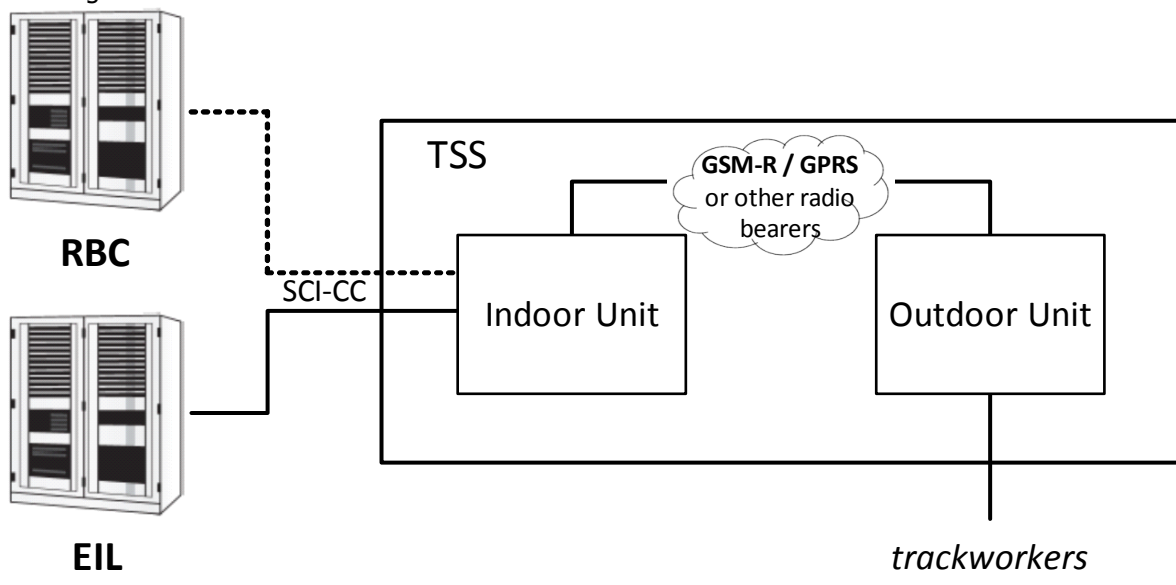
| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.101 | Head | <b>4.3.2 Conditional Emergency Stop Area</b>   |
| Eu.DK.104 | Info | CESA is also referred to as Escape area. When this area is activated, trains outside the area (Train 1) will receive a conditional emergency stop. Trains inside the area will not receive any stop messages. The following diagram displays the CESA scenario:  |
| Eu.DK.155 | Info | CESA scenario<br>  |
| Eu.DK.106 | Head | <b>4.4 Working Area</b>  |
| Eu.DK.107 | Info | Maintenance staff shall be protected technically against train traffic. A working area (WA) is a predefined area where maintenance work can be done safely. Maintenance staff will be able to operate objects (such as points, derailleurs, level crossings and tunnel gates) within an activated WA.  |
| Eu.DK.108 | Head | <b>4.4.1 Working Area Activation</b>   |
| Eu.DK.109 | Info | For activating the working area several steps are required: <ol style="list-style-type: none"> <li>1. The signaller activates the area according to a work order</li> <li>2. The interlocking system receives the activation command, and performs necessary actions to activate the area.</li> <li>3. The interlocking confirms that the area is activated.</li> <li>4. Maintainer confirms presence in the relevant area. This can for example be done with a hand held terminal. The confirmation results in sending the securing command to the interlocking system.</li> <li>5. The interlocking system receives the securing command, and performs necessary actions to secure the area.</li> <li>6. The interlocking confirms that the area is activated and secured.</li> </ol> When the working area becomes secured, the signaller will have the possibility to enable transitions to shunting mode. |
| Eu.DK.110 | Head | <b>4.4.2 Working Area Life Cycle</b>   |
| Eu.DK.111 | Info | A working area is considered as: <ul style="list-style-type: none"> <li>• 'activated' if the activation request is not rejected, until the working area becomes secured in the activation process</li> <li>• 'secured' if the activation of the working area is completed by confirmation from the maintainer</li> <li>• 'not activated' if the withdrawal of an activated or secured working area is completed</li> </ul>   |
| Eu.DK.112 | Head | <b>4.4.3 Extended Working Area</b>   |
| Eu.DK.113 | Info | An extended WA will typically consist of two or more adjacent working areas. If a second WA is activated nearby an activated WA it is considered as extended also when the WAs are not overlapping.  |
| Eu.DK.156 | Info | Example of Extended Working Areas<br>  |

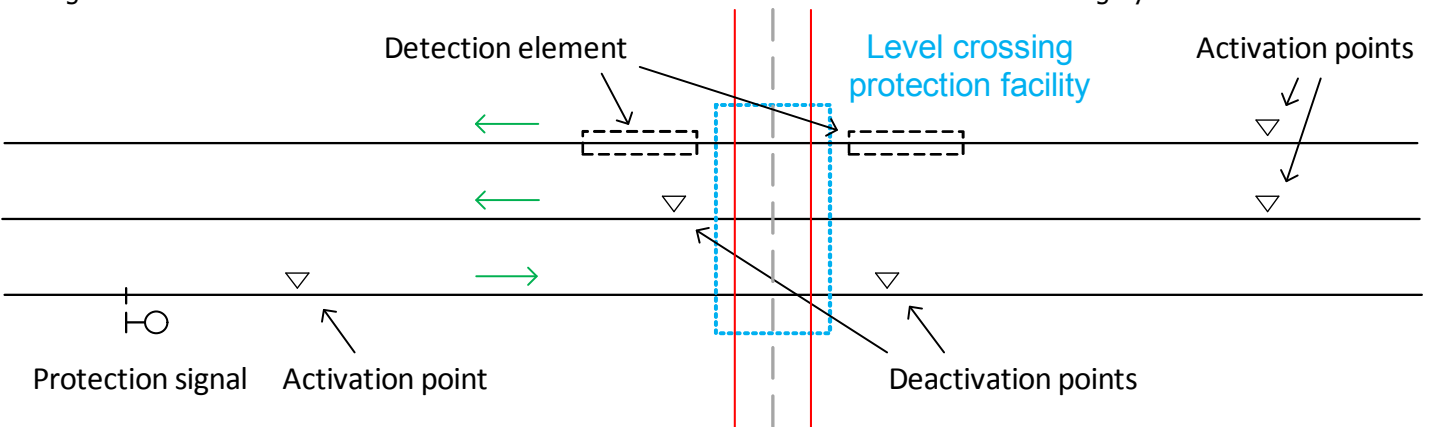
| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.60  | Head | <b>5 Adjacent Systems</b>  |
| Eu.DK.70  | Head | <b>5.1 Radio Block Centre</b>  |
| Eu.DK.71  | Info | This section contains domain knowledge related to the functionality between the interlocking system (EIL) and the Radio Block Centre (RBC).  |
| Eu.DK.116 | Head | <b>5.1.1 RBC General</b>   |
| Eu.DK.117 | Info | The RBC is a computer-based system that elaborates messages to be sent to the train on basis of information received from external trackside systems and on basis of information exchanged with the on-board subsystems. The main objective of these messages is to provide movement authorities to allow the safe movement of trains on the Railway infrastructure area under the responsibility of the RBC. The RBC is used in ETCS level 2 and level 3. In these levels there is a permanent communication between the train and the RBC. The RBC generates the movement authority (MA) considering dynamic and static data from train and track. The static data are part of planning procedure and include for example the position of points and Eurobalises as well as the speed restrictions or gradients on the track. The dynamic data are received by the RBC from the interlocking system and the train. |
| Eu.DK.157 | Info | <p>The relation between the RBC and the interlocking system</p> <p>The diagram illustrates the communication flow between the Radio Block Centre (RBC) and the Interlocking System (EIL). The RBC and EIL are connected via the RaSTA protocol, with the RBC sending requests and triggers to the EIL, and the EIL providing status information back to the RBC. The RBC is also connected to a GSM-R system, which communicates with the ETCS on-board unit on a train via the Euroradio protocol. This protocol involves the exchange of trackside data from the RBC to the GSM-R and train data from the GSM-R to the RBC. Additionally, the EIL sends command and monitor field elements to the track infrastructure, which includes a signal and a yellow diamond marker.</p>   |
| Eu.DK.119 | Head | <b>5.1.2 Definition of functions between the interlocking system and the RBC</b>   |
| Eu.DK.120 | Info | <b>Overlap release:</b><br>The release of overlap section on the track. Normally the EIL will release the overlap timer-driven. With ETCS L2 (or higher) the EIL may release the overlap after a signal which is reserved for overlap release by the ETCS if the RBC has sent permission for the release of the overlap and all of the internal conditions of the EIL are fulfilled.   |
| Eu.DK.121 | Info | <b>Route/sub-route request:</b><br>The request from the RBC to the interlocking system to lock a particular route or sub-route for a train. A sub-route may be set during start of mission up to the next signal.  |
| Eu.DK.122 | Info | <b>Route release:</b><br>The release of a route triggered by the RBC.  |
| Eu.DK.123 | Info | <b>Setting signals to dark:</b><br>Used in German LZB train control system and ETCS Level 2 (or higher). A line is divided into blocks. If there is no train in the entire line, the entry signal into the first block would be green. If the first block is occupied, the signal would be red. But if the first block is free and an LZB-or-ETCS-led train is approaching, the signal would set to dark and the train would continue just under the LZB/ETCS supervision. The purpose of using dark signals is to not let the driver get used to pass a red light signal.   |
| Eu.DK.124 | Info | <b>Route setting trigger:</b><br>The train runs over designated location and triggers the request for a route.   |

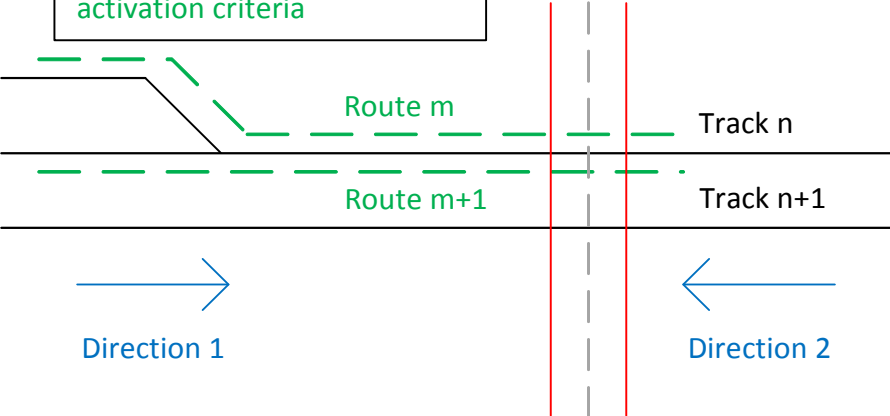
| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.125 | Info | <b>Blocking of mixed traffic in defined sections:</b><br>Functionality used to prevent meeting of passenger trains and freight trains in defined sections, such as tunnels. Operational requirement dictates that passenger trains and freight trains must not encounter in a single-tube (double-tracked) tunnel.<br>The German term for this functionality is 'Tunnelbegegnungsverbot', abbreviated as TBV.  |
| Eu.DK.126 | Info | <b>Group failure:</b><br>Field elements connected to an EIL may be partitioned into groups of elements due to the HW-architecture of an EIL. If the elements of a group are failed, the EIL sends a group failure message to the RBC in order to avoid a mass of single failure messages, individually for every single element.<br>If elements of more than one group are out of order, the EIL sends to the RBC a separate failure message for each group. |
| Eu.DK.383 | Head | <b>5.2 Centralised ETCS L1 Controller</b>  |
| Eu.DK.384 | Info | This section contains domain knowledge related to the functionality between the interlocking system (EIL) and the Centralised ETCS L1 Controller (CEC).  |
| Eu.DK.385 | Head | <b>5.2.1 CEC General</b>   |
| Eu.DK.386 | Info | The CEC receives status information from the interlocking system, in a similar way as the RBC receives such information. The CEC then determines, according to engineering data and internal logic, which balise groups should transmit which messages and sends the relevant telegrams into the corresponding Eurobalises via a balise driver.  |
| Eu.DK.387 | Info | The CEC can control the Eurobalises for a whole signalling area (one or several stations). It switches them according to its internal logic and status information from the interlocking system.   |
| Eu.DK.388 | Info | The CEC also incorporates information about temporary speed restrictions (TSRs). This information can be received from the Command Control system or from a dedicated system for TSR management.   |
| Eu.DK.389 | Head | <b>5.2.2 Interfaces</b>  |
| Eu.DK.390 | Info | The CEC receives status information from the interlocking system via the EULYNX interface SCI-RBC.   |
| Eu.DK.465 | Info | The status information received from the interlocking may contain only light signal status, or also include information about the status of other track elements (e.g. points, TVP sections, level crossings). The required status information depends on national specifications for the CEC.   |
| Eu.DK.391 | Info | The CEC receives TSR information from the CC system or the TSR management system via the interface SCI-CC.   |
| Eu.DK.392 | Info | The CEC controls balise drivers via an interface that is not standardised in EULYNX.   |

| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.393 | Info | <p>The diagram below shows the architectural location of the CEC and its interfaces</p>   |
| Eu.DK.394 | Head | <b>5.2.3 Switching order</b>  |
| Eu.DK.466 | Info | In most CEC systems, the balise groups will switch telegrams when the aspect of the associated light signal switches, or directly after the light signal aspect has been switched.  |
| Eu.DK.395 | Info | National specifications (and the followed signalling philosophy) may require that some balise groups may need to be switched before the associated light signals. If this principle is used, a distinction is made between pre-signal and post-signal balise groups.  |
| Eu.DK.396 | Info | Pre-Signal Balise Group are switched before the light signal, to which it is functionally associated, displays a more permissive aspect. The functionality of pre-signal balise groups ensures that a signal is not permitted to display a more permissive aspect in case of balise group failure. In this context, signal balise groups are generally treated as pre-signal balise groups. Signal balise groups are placed in the tracks close to a light signal to which they have a functional link. |
| Eu.DK.397 | Info | Post-Signal Balise Group are switched after or at the same moment the light signal displays the more permissive aspect already. If there is a failure in setting the post-signal balise groups, national rules decide whether the associated light signal can remain at the permissive aspect or not. In this context, infill balise groups are generally treated as post-signal balise groups. Infill balise group transmit information that is valid for a location in advance.                       |
| Eu.DK.400 | Head | <b>5.3 Trackworker Safety System</b>  |
| Eu.DK.401 | Info | This section contains domain knowledge related to the functionality between the interlocking system (EIL) and the Trackworker Safety System (TSS).  |
| Eu.DK.402 | Head | <b>5.3.1 TSS General</b>  |
| Eu.DK.403 | Info | Trackside Safety Systems provide warnings and can apply additional protection for trackside workers. The TSS collects information about the position of trains and rail vehicles from various sources, such as the electronic interlocking. The Control Unit of the TSS processes the train position information and generates a warning message when any train or rail vehicle reaches a trigger point on approach to a warning area.  |
| Eu.DK.404 | Head | <b>5.3.2 TSS architecture and interfaces</b>  |
| Eu.DK.405 | Info | The TSS in the context of EULYNX is an implementation of a signal controlled warning system (SCWS), as defined in [EN 16704-2-1], consisting of a Control Unit Indoor and a Control Unit Outdoor.   |

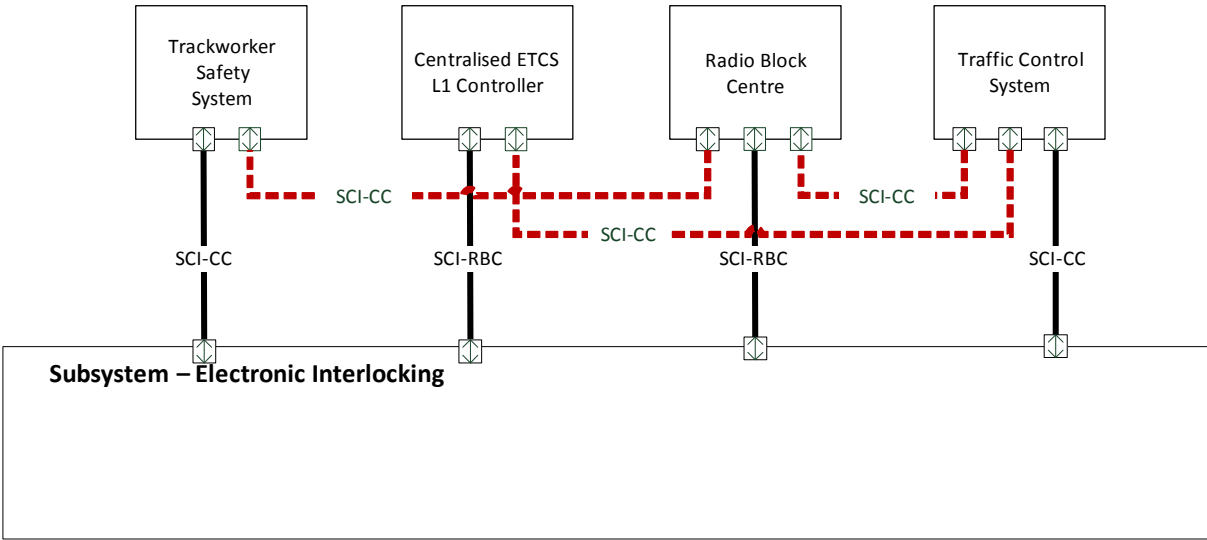




















| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.406 | Info | The Control Unit Outdoor can interface to trackside workers and warning units along the track. The interfaces and implementation of the Control Unit Outdoor are outside the scope of EULYNX. The EULYNX System interfaces only to the Control Unit Indoor, as part of the TSS.  |
| Eu.DK.407 | Info | <p>The diagram below shows the architecture of a TSS and its interfaces</p>  <p>The diagram illustrates the architecture of a TSS. On the left, there are two rack-mounted units labeled 'RBC' (top) and 'EIL' (bottom). A dashed line labeled 'SCI-CC' connects the RBC to the 'Indoor Unit' within a larger box labeled 'TSS'. A solid line connects the EIL to the 'Indoor Unit'. The 'Indoor Unit' is connected to the 'Outdoor Unit' within the TSS box via a cloud labeled 'GSM-R / GPRS or other radio bearers'. A line extends from the 'Outdoor Unit' to the label 'trackworkers' below it.</p> |
| Eu.DK.408 | Head | <b>5.3.3 Warning functions</b>   |
| Eu.DK.409 | Info | To perform the functions of a signal controlled warning system, the TSS receives status information about warning conditions from the interlocking system via a dedicated interface SCI-CC.  |
| Eu.DK.410 | Info | <p>Warning conditions include:</p> <ul style="list-style-type: none"> <li>• Routes set</li> <li>• Signal aspects</li> <li>• Positions of points</li> <li>• Track section occupancy</li> </ul>  |
| Eu.DK.411 | Info | The TSS may have an additional interface to the Radio Block Centre (RBC), also using the interface SCI-CC.   |
| Eu.DK.412 | Info | Additional warning conditions can be received either from the interlocking system or from the RBC, depending on the functional apportionment between these two systems.  |
| Eu.DK.413 | Info | <p>Additional warning conditions include:</p> <ul style="list-style-type: none"> <li>• Train location and speed</li> <li>• Train status</li> </ul>   |
| Eu.DK.414 | Head | <b>5.3.4 Influence functions</b>   |
| Eu.DK.415 | Info | In addition to providing warnings to trackside workers, the TSS can use influence functions to apply additional protection.  |
| Eu.DK.416 | Info | <p><b>Manage Working Areas</b><br/>The TSS can command the interlocking system to secure / unsecure working areas, to make sure workers are protected against trains in an identified area.</p>  |
| Eu.DK.417 | Info | <p><b>Set Signal to Stop</b><br/>In emergency situations, the TSS can command the interlocking system to set specific light signals to a Stop Aspect to stop trains from approaching a dangerous location.</p>   |
| Eu.DK.418 | Info | <p><b>Delay route setting</b><br/>The TSS can command the interlocking system to apply a delay when setting a route and clearing the associated route entry signal. This allows trackside workers additional time to vacate the track in those cases where the route entry signal is located close to the working location.</p>  |
| Eu.DK.434 | Head | <b>5.4 External Level Crossing System</b>  |
| Eu.DK.438 | Info | Systems to prevent collisions between trains and road users at level crossings are integrated to the interlocking system through the subsystem Level Crossing or through the adjacent system External Level Crossing System.   |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.439 | Info | The adjacent system External Level Crossing System is used to integrate level crossing systems for which the activation and deactivation logic is handled primarily inside the level crossing system, based on commands from the interlocking and on the status of connected (de)activation points and detection elements.   |
| Eu.DK.440 | Info | The adjacent system External Level Crossing System controls one level crossing as a single operational element. The External Level Crossing System contains a level crossing protection facility, as defined in section 6.5 (see Eu.DK.293).   |
| Eu.DK.441 | Info | <p>The figure below shows the main definitions of elements related to the External Level Crossing System.</p>  <p>The diagram illustrates the layout of a level crossing protection facility. It shows three horizontal tracks. A central section is enclosed in a blue dashed box labeled 'Level crossing protection facility'. This section is bounded by two vertical red lines labeled 'Deactivation points'. To the left of the facility, there are two green arrows pointing left, labeled 'Detection element'. To the right, there are two green arrows pointing right, labeled 'Activation points'. On the bottom track, there is a 'Protection signal' (H-O) and an 'Activation point' (inverted triangle). On the top track, there are two 'Deactivation points' (inverted triangles). A dashed line indicates the track centerline.</p> |
| Eu.DK.442 | Head | <b>5.4.1 Interacting functions</b>   |
| Eu.DK.300 | Info | <p>Interacting functions are performed in cooperation with the interlocking and related to activation or deactivation of the level crossing protection facility. The interlocking sends activation and deactivation commands to the External Level Crossing System. Multiple principles are used to activate or deactivate the protection facility of a level crossing:</p> <ul style="list-style-type: none"> <li>• Unconditional activation and deactivation</li> <li>• Track/route-related activation and deactivation</li> <li>• Prolonged activation</li> <li>• Control activation point</li> </ul>   |
| Eu.DK.445 | Head | <b>5.4.1.1 Unconditional activation and deactivation</b>   |
| Eu.DK.443 | Info | The start of the activation or deactivation sequence is directly triggered by a command from the interlocking. The unconditional activation (or deactivation) refers to all tracks of the level crossing. That means that the complete level crossing protection facility shall be activated (or deactivated) without conditions on track, direction or route.   |
| Eu.DK.444 | Info | <p>Activation or deactivation may be commanded based on one or more conditions in the interlocking. Examples of conditions leading to an unconditional activation are:</p> <ul style="list-style-type: none"> <li>• a request resulting from a command by the signaller</li> <li>• a request resulting from a command by the Radio Block Centre</li> </ul>   |
| Eu.DK.308 | Head | <b>5.4.1.2 Track/route-related activation and deactivation</b>   |
| Eu.DK.309 | Info | The interlocking commands the External Level Crossing System to expect train movement on a certain track or route. The External Level Crossing System evaluates if the conditions for activation are fulfilled and triggers the activation sequence of the level crossing protection facility as soon as it detects a train on an activated activation point corresponding to the commanded track or route. If no activation point exists for a certain track or route, the activation sequence of the protection facility is triggered immediately.   |
| Eu.DK.310 | Info | The External Level Crossing System triggers the deactivation sequence of the level crossing protection facility when it detects a train on a deactivation point or on a deactivation element corresponding to the commanded track or route (and no trigger for activation is present).   |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.350 | Info | <p>The figure below shows the main definitions related to track/route-related activation and deactivation.</p> <div data-bbox="430 189 825 310" style="border: 1px solid black; padding: 5px; margin-bottom: 10px;"> <p>Note: Each route can have a different speed and different activation criteria</p> </div>   |
| Eu.DK.311 | Head | <b>5.4.1.3 Prolonged activation</b>  |
| Eu.DK.312 | Info | The interlocking commands the External Level Crossing System to remain activated, i.e. to maintain the protection of the level crossing protection area.   |
| Eu.DK.313 | Head | <b>5.4.1.4 Control activation point</b>  |
| Eu.DK.314 | Info | The interlocking commands the External Level Crossing System to activate or deactivate a certain activation point. The External Level Crossing System triggers the activation sequence of the level crossing protection facility as soon as it detects a train on the selected activation point.   |
| Eu.DK.315 | Head | <b>5.4.2 Autonomous functions</b>  |
| Eu.DK.316 | Info | Autonomous functions are performed inside the External Level Crossing System without interaction with the interlocking.  |
| Eu.DK.317 | Head | <b>5.4.2.1 Autonomous activation and deactivation</b>  |
| Eu.DK.318 | Info | The External Level Crossing System triggers the activation sequence of the level crossing protection facility as soon as it detects a train on an activation point configured for autonomous activation.   |
| Eu.DK.319 | Info | The External Level Crossing System triggers the deactivation sequence of the level crossing protection facility as soon as it detects a train on a deactivation point or on a detection element configured for autonomous activation (and no trigger for activation is present).   |
| Eu.DK.320 | Head | <b>5.4.3 Combinations</b>  |
| Eu.DK.321 | Info | It is possible for one External Level Crossing System to use several different principles of activation and deactivation. Depending on different tracks, routes and directions, the level crossing protection facility can be activated by unconditional activation commanded by the interlocking, by a track/route-related activation commanded by the interlocking, by an activation point commanded by the interlocking and/or by an autonomous activation point not commanded by the interlocking. |
| Eu.DK.322 | Info | At a level crossing covering more than one track, it is possible for several activations to occur (partly) simultaneously. It is the responsibility of the External Level Crossing System to supervise the ' <i>most protective activation envelope</i> '; meaning the level crossing protection facility shall be activated as soon as required by one activation and remain activated until all activations have been concluded by a corresponding deactivation (either commanded or autonomous).    |
| Eu.DK.323 | Head | <b>5.4.4 Auxiliary functions</b>   |
| Eu.DK.324 | Info | Auxiliary functions are performed in cooperation with the interlocking, but not directly related to the activation or deactivation of the level crossing protection facility. The interlocking sends auxiliary commands to the External Level Crossing System.   |
| Eu.DK.330 | Head | <b>5.4.4.1 Set protection signals</b>  |
| Eu.DK.331 | Info | The interlocking can command the External Level Crossing System to set its protection signals to a stop aspect. This may be used when a signaller observes via cameras or by other means notices a dangerous situation on the level crossing protection area.  |
| Eu.DK.448 | Head | <b>5.4.5 Statuses</b>  |
| Eu.DK.449 | Info | The External Level Crossing System informs the interlocking of its status, based on different principles:  |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.450 | Info | <b>Functional status</b><br>This message is used for the statuses of the External Level Crossing System which are required within the interlocking logic.  |
| Eu.DK.451 | Info | <b>Monitoring status</b><br>This message is used for the statuses of the External Level Crossing System which are required for display to the signaller.   |
| Eu.DK.452 | Info | <b>Failure status</b><br>This message is used when a failure occurred or is revoked.   |
| Eu.DK.453 | Info | <b>Obstacle detection status</b><br>This message is used to report an obstacle detected inside the level crossing protection area.   |
| Eu.DK.454 | Info | <b>Detection element status</b><br>This message is used to report the occupancy status of detection elements.  |
| Eu.DK.455 | Info | <b>Status of activation point</b><br>This message is used to report the status of activations points.  |
| Eu.DK.456 | Head | <b>5.4.6 Command admissibility</b>   |
| Eu.DK.457 | Info | The monitoring of activation and deactivation is in the logic of the External Level Crossing System. National requirements can request that the subsystem Electronic Interlocking must check the admissibility of commands received from the Traffic Control System which cause a change in the state of the External Level Crossing System. This is a feasibility check of the commands coming from the signaller.  |
| Eu.DK.458 | Info | If a command of the signaller is permitted in the current state of the External Level Crossing System, the signaller receives a confirmation with a positive processing message. If a command of the signaller is not permitted in the current state of the External Level Crossing System, the signaller receives a negative processing message and the command is rejected thereby.  |
| Eu.DK.459 | Info | To reduce the processing time of a command of the signaller and to avoid the forwarding of the admissibility check to the External Level Crossing System, the External Level Crossing System sends the scope of the currently permitted and not permitted signaller commands with each relevant change of state.   |
| Eu.DK.460 | Info | This command admissibility has to be evaluated by the subsystem Electronic Interlocking when a signaller command is received from the Traffic Control System in order to accept or to reject this command.   |
| Eu.DK.554 | Head | <b>5.5 Traffic Control System</b>  |
| Eu.DK.555 | Info | This section contains domain knowledge related to the functionality between the interlocking system (EIL) and the Traffic Control System (TCS).  |
| Eu.DK.556 | Head | <b>5.5.1 TCS General</b>   |
| Eu.DK.557 | Info | In the EULYNX System reference architecture, three systems are considered to be part of the Traffic Control System: <ul style="list-style-type: none"> <li>• Command Control System</li> <li>• Automatic Route Setting System</li> <li>• Train Describer</li> </ul>  |
| Eu.DK.558 | Info | Although train operations as well as graphic symbols of infrastructure elements are different on European level, information like the states of infrastructure elements (for example locked, occupied, vacant) may be exchanged between the EULYNX System and the Command Control System as a generic standard.  |
| Eu.DK.559 | Info | There may be multiple scenarios for interfacing the EULYNX System and/or the Radio Block Centre to the Traffic Control System, including: <ul style="list-style-type: none"> <li>• single interface to the EULYNX System</li> <li>• separate interfaces to the EULYNX System and to the Radio Block Centre</li> <li>• single interface to the EULYNX System, which may integrate the functions of both the interlocking system and the Radio Block Centre</li> <li>• single interface to the EULYNX System, while the Radio Block Centre interfaces separately to the EULYNX System (without an interface to the TCS)</li> </ul> |
| Eu.DK.560 | Info | The SCI-CC interface specification will be specified in a common format and structure, regardless whether it is used for interfacing with the EULYNX System, the Radio Block Centre or the Centralised ETCS L1 Controller.   |
| Eu.DK.561 | Info | The SCI-CC interface is not intended for communication between two Traffic Control Systems.  |
| Eu.DK.562 | Info | It is foreseen that individual implementations of the SCI-CC interface protocol will use a full set or a partial subset of the application data, depending on the applied scenario, as defined by national specifications.   |
| Eu.DK.424 | Head | <b>5.6 EULYNX Interfaces between adjacent systems</b>  |
| Eu.DK.425 | Info | Certain EULYNX interface specifications can also be used to directly connect two adjacent systems to each other.   |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.426 | Info | The SCI-CC interface specification can also be applied for connecting the Traffic Control System directly to the following adjacent systems: <ul style="list-style-type: none"> <li>• the Radio Block Centre</li> <li>• the Centralised ETCS L1 Controller</li> </ul>  |
| Eu.DK.427 | Info | The SCI-CC interface specification can also be applied for connecting the Trackworker Safety System directly to the following adjacent systems: <ul style="list-style-type: none"> <li>• the Radio Block Centre</li> </ul>   |
| Eu.DK.428 | Info | In such cases the functional apportionment must be completed from the perspective of the adjacent system by the system integrator.   |
| Eu.DK.429 | Info | The diagram below displays the use of EULYNX interfaces between adjacent systems<br>----- EULYNX interface applied between adjacent systems<br> <p>The diagram illustrates the connectivity between four systems and a subsystem. At the top, four boxes represent: Trackworker Safety System, Centralised ETCS L1 Controller, Radio Block Centre, and Traffic Control System. Below these is a large box labeled 'Subsystem - Electronic Interlocking'. Solid lines connect each system to the subsystem: Trackworker Safety System to SCI-CC, Centralised ETCS L1 Controller to SCI-RBC, Radio Block Centre to SCI-RBC, and Traffic Control System to SCI-CC. Dashed red lines represent EULYNX interfaces: between Trackworker Safety System and Centralised ETCS L1 Controller (SCI-CC), between Centralised ETCS L1 Controller and Radio Block Centre (SCI-CC), between Radio Block Centre and Traffic Control System (SCI-CC), and between Trackworker Safety System and Radio Block Centre (SCI-CC).</p> |
| Eu.DK.72  | Head | <b>6 Elements</b>  |
| Eu.DK.73  | Info | This section contains domain knowledge related to individual elements.   |
| Eu.DK.174 | Head | <b>6.1 Light Signals</b>   |
| Eu.DK.249 | Info | Wayside light signal and indicator lamps are integrated to the interlocking system through the subsystem Light Signal.   |
| Eu.DK.248 | Info | The subsystem Light Signal controls one light signal as a single operational element.  |
| Eu.DK.237 | Head | <b>6.1.1 Signal aspect table</b>   |
| Eu.DK.250 | Info | Since signal aspects are different on European level, the aspects are managed on an abstract level and defined through the signal aspect table [Eu.Doc.37].  |
| Eu.DK.251 | Info | In the signal aspect table, all national signal aspects are assigned to generic signal aspect names.   |
| Eu.DK.252 | Info | For each generic signal aspect name, the signal aspect table defines a value of the signal vector.   |
| Eu.DK.253 | Info | The signal vector value is the expression of signal aspects used in the communication between the subsystem Light Signal and the subsystem Electronic Interlocking.  |
| Eu.DK.239 | Head | <b>6.1.2 Signal vector</b>   |
| Eu.DK.254 | Info | The signal vector consists of 6 bytes of information. The following diagram displays the structure of the signal vector.   |

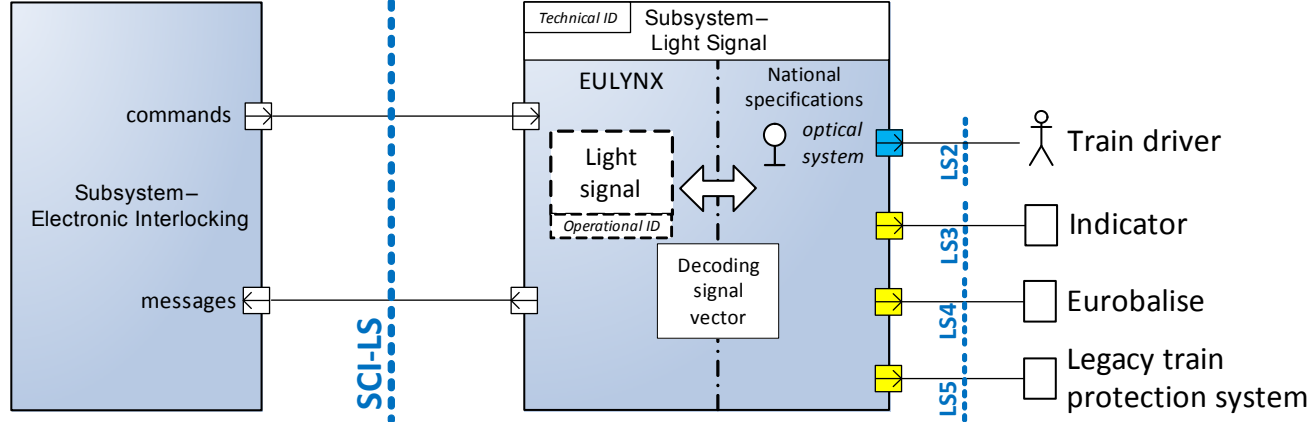
| ID  | Type  | Domain knowledge  |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
|---|---|---|---|---|---|----------|----------|----------|---|---|---|---|---|---|-------------------|--------------------------------|------------------|-------------------------------|----------------------|-----------------------------------|
| Eu.DK.255   | Info  | <p style="text-align: center;"><b>Signal vector</b></p> <div style="border: 1px dashed black; padding: 10px; margin: 10px auto; width: fit-content;"> <table style="width: 100%; text-align: center; border-collapse: collapse;"> <tr> <td style="width: 16.6%;">1st byte</td> <td style="width: 16.6%;">2nd byte</td> <td style="width: 16.6%;">3rd byte</td> <td style="width: 16.6%;">4th byte</td> <td style="width: 16.6%;">5th byte</td> <td style="width: 16.6%;">6th byte</td> </tr> <tr> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>basic aspect type</td> <td>extension of basic aspect type</td> <td>speed indicators</td> <td>speed indicator announcements</td> <td>direction indicators</td> <td>direction indicator announcements</td> </tr> </table> </div> | 1st byte  | 2nd byte  | 3rd byte  | 4th byte | 5th byte | 6th byte |  |  |  |  |  |  | basic aspect type | extension of basic aspect type | speed indicators | speed indicator announcements | direction indicators | direction indicator announcements |
| 1st byte  | 2nd byte  | 3rd byte  | 4th byte  | 5th byte  | 6th byte  |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
|  |  |    |  |  |  |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| basic aspect type   | extension of basic aspect type  | speed indicators  | speed indicator announcements   | direction indicators  | direction indicator announcements   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.256   | Info  | The coding of the bytes of the signal vector corresponds to the coding used in the signal aspect table [Eu.Doc.37] and to the telegrams Command "Indicate Signal Aspect" and Message "Indicated Signal Aspect" in the Interface specification SCI-LS [Eu.Doc.33].   |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.257   | Info  | <p>The 6 bytes of the signal vector represent the following information:</p> <ul style="list-style-type: none"> <li>• First byte: code for basic aspect types</li> <li>• Second byte: code for extension of basic aspect types</li> <li>• Third byte: speed indicators</li> <li>• Fourth byte: speed indicator announcements</li> <li>• Fifth byte: direction indicators</li> <li>• Sixth byte: direction indicator announcements</li> </ul>  |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.258   | Info  | The meaning of each byte value and the relation to corresponding national signal aspects can be found in the signal aspect table [Eu.Doc.37].   |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.259   | Info  | The bytes of the signal vector are independent. As an example, the speed indicator byte can take any value described in the signal aspect table, independent of the value of the bytes for the basic aspect, extension, speed indicator announcements and direction indicators. Configuration and engineering data define which combinations of the signal vector byte values constitute a valid signal aspect at an individual signal.   |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.260   | Head  | <b>6.1.3 Commanding the signal aspect</b>   |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.261   | Info  | The subsystem Electronic Interlocking sends the signal vector corresponding to the desired signal aspect to the subsystem Light Signal. It can send additional information that specifies additional rules to be taken into account when transforming the signal vector into a signal aspect.   |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.262   | Info  | <p>The subsystem Light Signal decodes the received signal vector and transforms it into a signal aspect. National specifications govern the interpretation of the signal vector byte values and any additional rules to drive:</p> <ul style="list-style-type: none"> <li>• Signal optics</li> <li>• Indicators</li> <li>• Eurobalises</li> <li>• Legacy train protection systems</li> </ul>  |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |
| Eu.DK.263   | Info  | The national specifications that are needed to drive the above mentioned components shall be covered by the configuration of the national part on the subsystem Light Signal.   |   |   |   |          |          |          |   |   |   |   |   |   |                   |                                |                  |                               |                      |                                   |

| ID | Type | Domain knowledge |
|----|------|------------------|
|----|------|------------------|

Eu.DK.264

Info

The diagram below shows the main definitions regarding the subsystem Light Signal.



Eu.DK.268

Info

**Examples of simple signal vector values**

Eu.DK.280

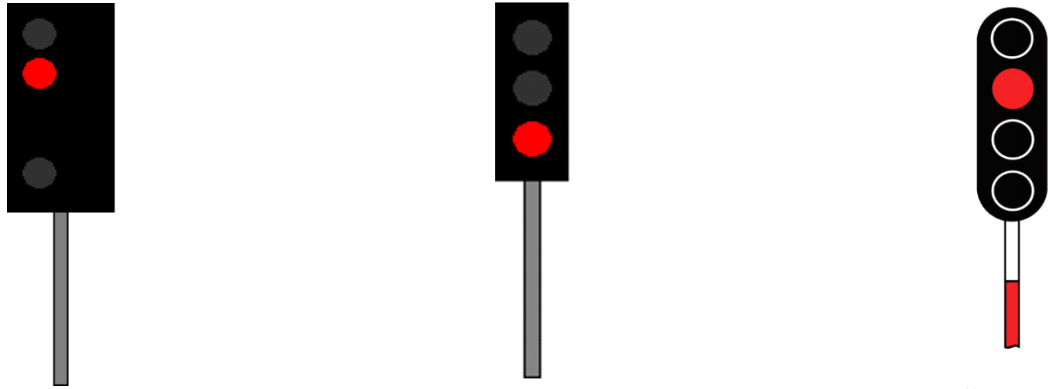
Info

Example 1: Stop / Danger (1)

| 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte            | 3 <sup>rd</sup> byte | 4 <sup>th</sup> byte          | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte              |
|----------------------|---------------------------------|----------------------|-------------------------------|----------------------|-----------------------------------|
| Basic aspect type    | Extension of basic aspect types | Speed indicators     | Speed indicator announcements | Direction indicators | Direction indicator announcements |
| 0x01                 | 0xFF                            | 0xFF                 | 0xFF                          | 0xFF                 | 0xFF                              |

Eu.DK.282

Info



DB: Hp 0

NR: Red/Stop/On

SŽ: SZ1

Eu.DK.283

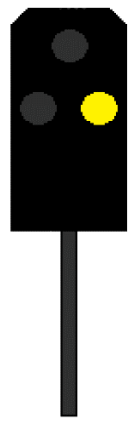


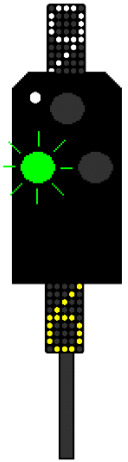
Info

Example 2: Approach / Caution




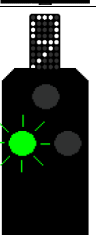



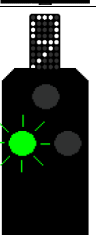



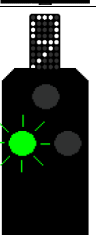

| 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte            | 3 <sup>rd</sup> byte | 4 <sup>th</sup> byte          | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte              |
|----------------------|---------------------------------|----------------------|-------------------------------|----------------------|-----------------------------------|
| Basic aspect type    | Extension of basic aspect types | Speed indicators     | Speed indicator announcements | Direction indicators | Direction indicator announcements |
| 0x07                 | 0xFF                            | 0xFF                 | 0xFF                          | 0xFF                 | 0xFF                              |

Eu.DK.284

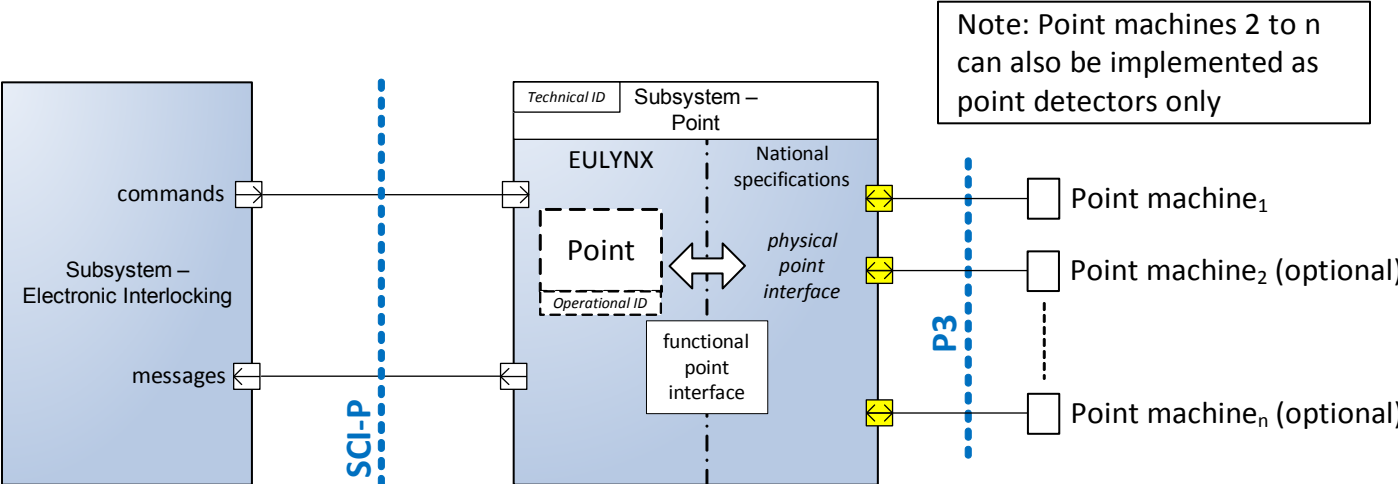
Info

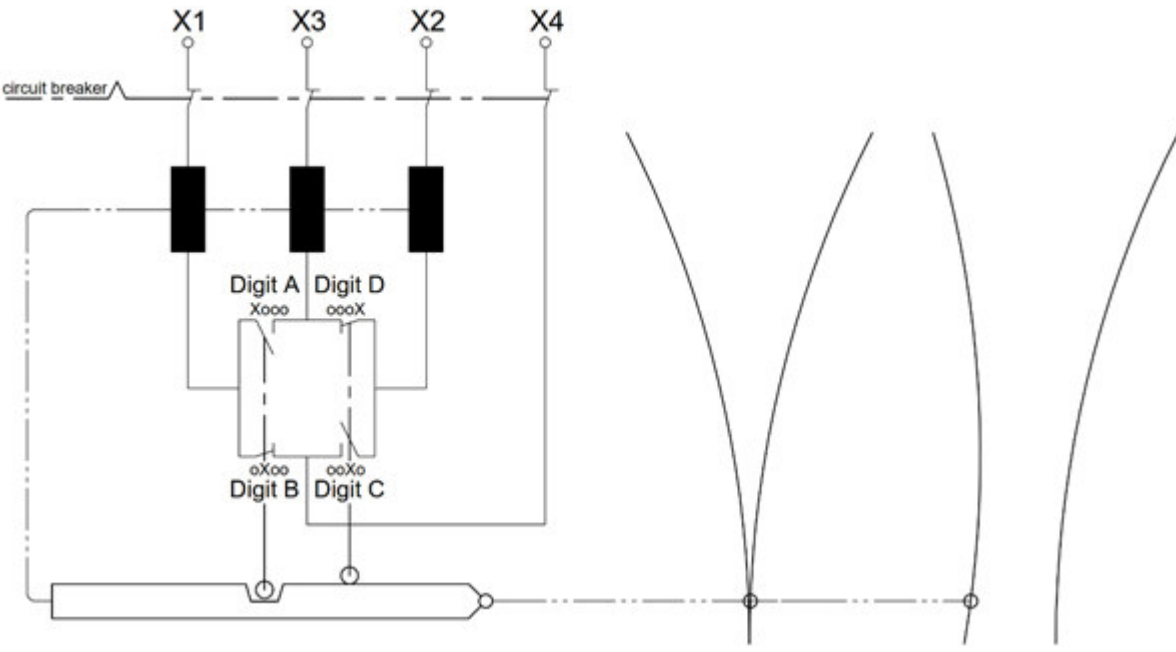
| ID                   | Type                            | Domain knowledge   |                               |                      |                                   |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
|----------------------|---------------------------------|--|-------------------------------|----------------------|-----------------------------------|----------------------|----------------------|----------------------|-------------------|---------------------------------|------------------|-------------------------------|----------------------|-----------------------------------|------|------|------|------|------|------|
|                      |                                 | <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;">  <p>DB: Ks 2</p> </div> <div style="text-align: center;">  <p>NR: Yellow</p> </div> <div style="text-align: center;">  <p>SŽ: SZ3</p> </div> </div>  |                               |                      |                                   |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| Eu.DK.269            | Info                            | <b>Examples of compound signal vector values</b>   |                               |                      |                                   |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| Eu.DK.285            | Info                            | <p>Example 3: Flashing clear (2) with speed indicator and speed indicator announcement<br/>90 km/h in advance of signal, 60km/h at next signal</p> <table border="1" data-bbox="365 772 1498 947"> <thead> <tr> <th>1<sup>st</sup> byte</th> <th>2<sup>nd</sup> byte</th> <th>3<sup>rd</sup> byte</th> <th>4<sup>th</sup> byte</th> <th>5<sup>th</sup> byte</th> <th>6<sup>th</sup> byte</th> </tr> </thead> <tbody> <tr> <td>Basic aspect type</td> <td>Extension of basic aspect types</td> <td>Speed indicators</td> <td>Speed indicator announcements</td> <td>Direction indicators</td> <td>Direction indicator announcements</td> </tr> <tr> <td>0x06</td> <td>0xFF</td> <td>0x09</td> <td>0x06</td> <td>0xFF</td> <td>0xFF</td> </tr> </tbody> </table> | 1 <sup>st</sup> byte          | 2 <sup>nd</sup> byte | 3 <sup>rd</sup> byte              | 4 <sup>th</sup> byte | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte | Basic aspect type | Extension of basic aspect types | Speed indicators | Speed indicator announcements | Direction indicators | Direction indicator announcements | 0x06 | 0xFF | 0x09 | 0x06 | 0xFF | 0xFF |
| 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte            | 3 <sup>rd</sup> byte   | 4 <sup>th</sup> byte          | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte              |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| Basic aspect type    | Extension of basic aspect types | Speed indicators   | Speed indicator announcements | Direction indicators | Direction indicator announcements |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| 0x06                 | 0xFF                            | 0x09   | 0x06                          | 0xFF                 | 0xFF                              |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| Eu.DK.286            | Info                            | <div style="text-align: center;">  </div> <p>DB: Ks 1 with Zs 3, Zs 3v and <i>Zusatzlicht</i> (indicating shortened braking distance)</p>   |                               |                      |                                   |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| Eu.DK.287            | Info                            | <p>Example 4: Approach / Caution with indicator 'no overlap'</p> <table border="1" data-bbox="365 1577 1498 1751"> <thead> <tr> <th>1<sup>st</sup> byte</th> <th>2<sup>nd</sup> byte</th> <th>3<sup>rd</sup> byte</th> <th>4<sup>th</sup> byte</th> <th>5<sup>th</sup> byte</th> <th>6<sup>th</sup> byte</th> </tr> </thead> <tbody> <tr> <td>Basic aspect type</td> <td>Extension of basic aspect types</td> <td>Speed indicators</td> <td>Speed indicator announcements</td> <td>Direction indicators</td> <td>Direction indicator announcements</td> </tr> <tr> <td>0x07</td> <td>0x13</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </tbody> </table>   | 1 <sup>st</sup> byte          | 2 <sup>nd</sup> byte | 3 <sup>rd</sup> byte              | 4 <sup>th</sup> byte | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte | Basic aspect type | Extension of basic aspect types | Speed indicators | Speed indicator announcements | Direction indicators | Direction indicator announcements | 0x07 | 0x13 | 0xFF | 0xFF | 0xFF | 0xFF |
| 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte            | 3 <sup>rd</sup> byte   | 4 <sup>th</sup> byte          | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte              |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| Basic aspect type    | Extension of basic aspect types | Speed indicators   | Speed indicator announcements | Direction indicators | Direction indicator announcements |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| 0x07                 | 0x13                            | 0xFF   | 0xFF                          | 0xFF                 | 0xFF                              |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |
| Eu.DK.288            | Info                            |  |                               |                      |                                   |                      |                      |                      |                   |                                 |                  |                               |                      |                                   |      |      |      |      |      |      |



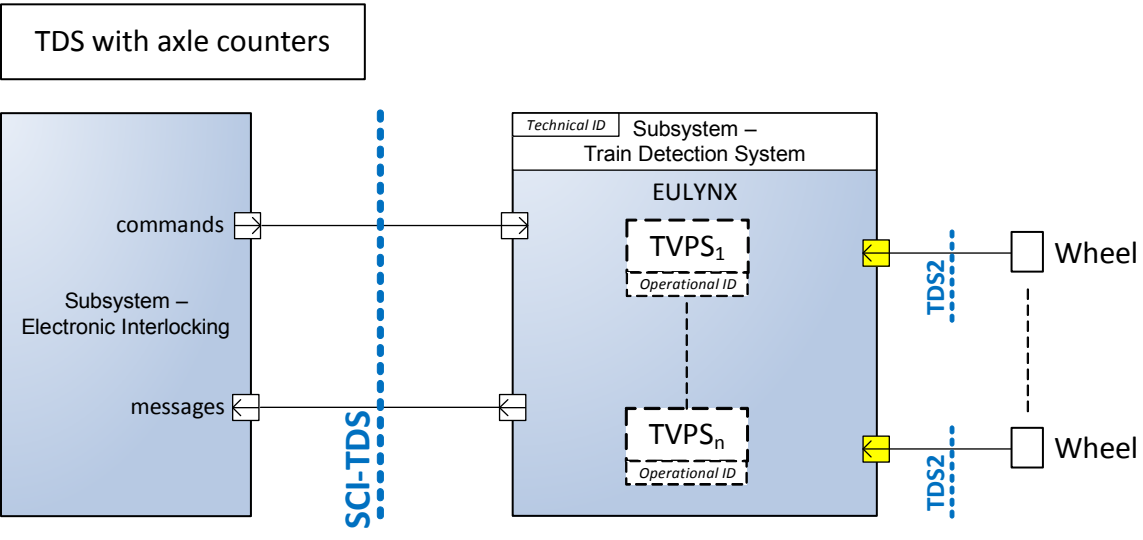
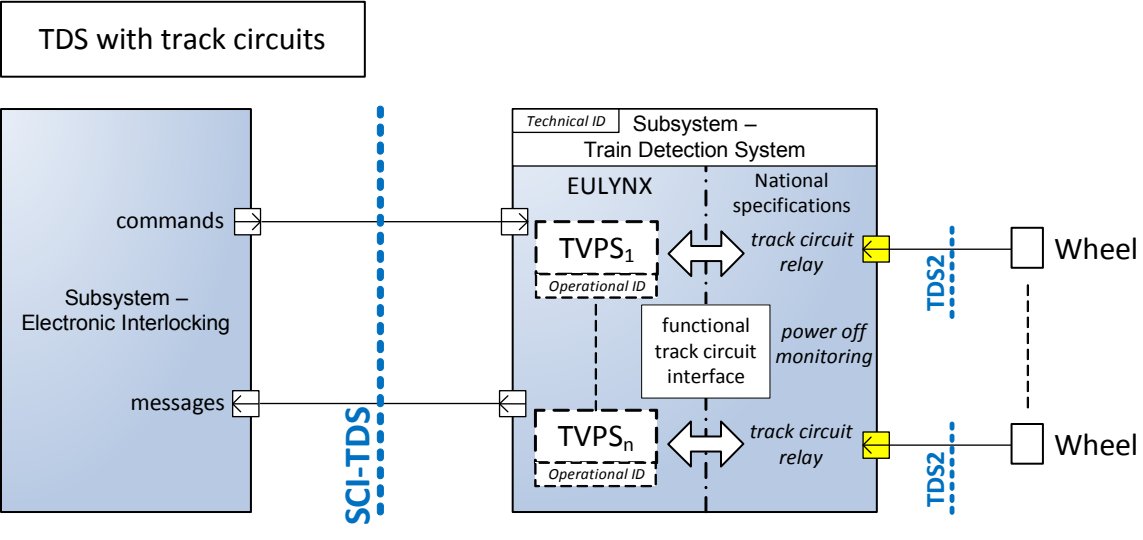
| ID                   | Type  | Domain knowledge  |   |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
|----------------------|---|---|---|----------------------|----------------------|---|----------------------|---|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|--|--|----|---|------|------|------|------|------|------|---|-----|----|---|------|------|------|------|------|------|---|----|----|---|------|------|------|------|------|------|---|----|----|---|------|------|------|------|------|------|---|-------------|
|                      |   |  <p data-bbox="362 604 557 634">SŽ: SZ3 with SZ23</p>  |   |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| Eu.DK.265            | Head  | <b>6.1.4 Degradation</b>  |   |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| Eu.DK.266            | Info  | If, for example because of a lamp failure, a light signal cannot show the commanded signal aspect, it must show another valid signal aspect. The alternative aspect shown shall always give a more restrictive instruction to the train driver. The choice of alternative signal aspects to be used in case of degradation is governed by national specifications and must be included in the configuration of the national part on the subsystem Light Signal.   |   |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| Eu.DK.267            | Info  | The process of degradation takes place within the subsystem Light Signal, based on information that has been configured. After applying degradation, the subsystem Light Signal reports to the subsystem Electronic Interlocking the signal aspect that is indicated to the train driver. There is no further interaction with the interlocking.  |   |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| Eu.DK.270            | Info  | <b>Example of degradation</b>   |   |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| Eu.DK.281            | Info  | <table border="1" data-bbox="362 976 1498 1816"> <thead> <tr> <th data-bbox="362 976 540 1117">Aspect index</th> <th data-bbox="540 976 1053 1117">Signal vector value</th> <th data-bbox="1053 976 1231 1117">Example aspect</th> <th data-bbox="1231 976 1498 1117">When not available, degrade to aspect index</th> </tr> <tr> <td></td> <td> <table border="1" data-bbox="557 1012 1044 1087"> <tr> <td>1<sup>st</sup> byte</td> <td>2<sup>nd</sup> byte</td> <td>3<sup>rd</sup> byte</td> <td>4<sup>th</sup> byte</td> <td>5<sup>th</sup> byte</td> <td>6<sup>th</sup> byte</td> </tr> </table> </td> <td></td> <td></td> </tr> </thead> <tbody> <tr> <td data-bbox="362 1117 540 1281">#1</td> <td data-bbox="540 1117 1053 1281"> <table border="1" data-bbox="557 1150 1044 1186"> <tr> <td>0x01</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> </td> <td data-bbox="1053 1117 1231 1281">  </td> <td data-bbox="1231 1117 1498 1281">N/A</td> </tr> <tr> <td data-bbox="362 1281 540 1444">#2</td> <td data-bbox="540 1281 1053 1444"> <table border="1" data-bbox="557 1314 1044 1350"> <tr> <td>0x07</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> </td> <td data-bbox="1053 1281 1231 1444">  </td> <td data-bbox="1231 1281 1498 1444">#1</td> </tr> <tr> <td data-bbox="362 1444 540 1663">#3</td> <td data-bbox="540 1444 1053 1663"> <table border="1" data-bbox="557 1478 1044 1514"> <tr> <td>0x05</td> <td>0xFF</td> <td>0x09</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> </td> <td data-bbox="1053 1444 1231 1663">  </td> <td data-bbox="1231 1444 1498 1663">#2</td> </tr> <tr> <td data-bbox="362 1663 540 1816">#4</td> <td data-bbox="540 1663 1053 1816"> <table border="1" data-bbox="557 1696 1044 1732"> <tr> <td>0x04</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> </td> <td data-bbox="1053 1663 1231 1816">  </td> <td data-bbox="1231 1663 1498 1816">#2 (not #3)</td> </tr> </tbody> </table> | Aspect index                                | Signal vector value  | Example aspect       | When not available, degrade to aspect index |                      | <table border="1" data-bbox="557 1012 1044 1087"> <tr> <td>1<sup>st</sup> byte</td> <td>2<sup>nd</sup> byte</td> <td>3<sup>rd</sup> byte</td> <td>4<sup>th</sup> byte</td> <td>5<sup>th</sup> byte</td> <td>6<sup>th</sup> byte</td> </tr> </table> | 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte | 3 <sup>rd</sup> byte | 4 <sup>th</sup> byte | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte |  |  | #1 | <table border="1" data-bbox="557 1150 1044 1186"> <tr> <td>0x01</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> | 0x01 | 0xFF | 0xFF | 0xFF | 0xFF | 0xFF |  | N/A | #2 | <table border="1" data-bbox="557 1314 1044 1350"> <tr> <td>0x07</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> | 0x07 | 0xFF | 0xFF | 0xFF | 0xFF | 0xFF |  | #1 | #3 | <table border="1" data-bbox="557 1478 1044 1514"> <tr> <td>0x05</td> <td>0xFF</td> <td>0x09</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> | 0x05 | 0xFF | 0x09 | 0xFF | 0xFF | 0xFF |  | #2 | #4 | <table border="1" data-bbox="557 1696 1044 1732"> <tr> <td>0x04</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table> | 0x04 | 0xFF | 0xFF | 0xFF | 0xFF | 0xFF |  | #2 (not #3) |
| Aspect index         | Signal vector value   | Example aspect  | When not available, degrade to aspect index |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
|                      | <table border="1" data-bbox="557 1012 1044 1087"> <tr> <td>1<sup>st</sup> byte</td> <td>2<sup>nd</sup> byte</td> <td>3<sup>rd</sup> byte</td> <td>4<sup>th</sup> byte</td> <td>5<sup>th</sup> byte</td> <td>6<sup>th</sup> byte</td> </tr> </table> | 1 <sup>st</sup> byte  | 2 <sup>nd</sup> byte                        | 3 <sup>rd</sup> byte | 4 <sup>th</sup> byte | 5 <sup>th</sup> byte                        | 6 <sup>th</sup> byte |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte  | 3 <sup>rd</sup> byte  | 4 <sup>th</sup> byte                        | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| #1                   | <table border="1" data-bbox="557 1150 1044 1186"> <tr> <td>0x01</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table>   | 0x01  | 0xFF  | 0xFF                 | 0xFF                 | 0xFF  | 0xFF                 |    | N/A                  |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| 0x01                 | 0xFF  | 0xFF  | 0xFF  | 0xFF                 | 0xFF                 |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| #2                   | <table border="1" data-bbox="557 1314 1044 1350"> <tr> <td>0x07</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table>   | 0x07  | 0xFF  | 0xFF                 | 0xFF                 | 0xFF  | 0xFF                 |    | #1                   |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| 0x07                 | 0xFF  | 0xFF  | 0xFF  | 0xFF                 | 0xFF                 |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| #3                   | <table border="1" data-bbox="557 1478 1044 1514"> <tr> <td>0x05</td> <td>0xFF</td> <td>0x09</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table>   | 0x05  | 0xFF  | 0x09                 | 0xFF                 | 0xFF  | 0xFF                 |    | #2                   |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| 0x05                 | 0xFF  | 0x09  | 0xFF  | 0xFF                 | 0xFF                 |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| #4                   | <table border="1" data-bbox="557 1696 1044 1732"> <tr> <td>0x04</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> </tr> </table>   | 0x04  | 0xFF  | 0xFF                 | 0xFF                 | 0xFF  | 0xFF                 |    | #2 (not #3)          |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| 0x04                 | 0xFF  | 0xFF  | 0xFF  | 0xFF                 | 0xFF                 |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |
| Eu.DK.271            | Head  | <b>6.1.4.1 Lamp dependent degradation</b>   |   |                      |                      |   |                      |   |                      |                      |                      |                      |                      |                      |  |  |    |   |      |      |      |      |      |      |   |     |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |    |    |   |      |      |      |      |      |      |   |             |

| ID         | Type                 | Domain knowledge   |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
|------------|----------------------|--|----------------------|----------------------|----------------------|----------------------|---|--|--|---|----------------------|----------------------|----------------------|----------------------|----------------------|----------------------|----|------|------|------|------|------|------|-----|----|------|------|------|------|------|------|----|----|------|------|------|------|------|------|---|----|------|------|------|------|------|------|---|----|------|------|------|------|------|------|---|
| Eu.DK.272  | Info                 | If a signal aspect consists of more than one lamp, the degradation can depend on individual lamp failures.   |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.273  | Info                 | <b>Example of lamp dependent degradation</b>   |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.289  | Info                 | <table border="1"> <thead> <tr> <th rowspan="2">Aspect nr.</th> <th colspan="6">Signal vector value</th> <th rowspan="2">When not available, degrade to aspect nr.</th> </tr> <tr> <th>1<sup>st</sup> byte</th> <th>2<sup>nd</sup> byte</th> <th>3<sup>rd</sup> byte</th> <th>4<sup>th</sup> byte</th> <th>5<sup>th</sup> byte</th> <th>6<sup>th</sup> byte</th> </tr> </thead> <tbody> <tr> <td>#1</td> <td>0x01</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>N/A</td> </tr> <tr> <td>#2</td> <td>0x07</td> <td>0xFF</td> <td>0x06</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>#1</td> </tr> <tr> <td>#3</td> <td>0x07</td> <td>0xFF</td> <td>0x09</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>#1 or #2, depending on which lamp fails</td> </tr> <tr> <td>#4</td> <td>0x05</td> <td>0xFF</td> <td>0x06</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>#2 or #1, depending on which lamp fails</td> </tr> <tr> <td>#5</td> <td>0x05</td> <td>0xFF</td> <td>0x09</td> <td>0xFF</td> <td>0xFF</td> <td>0xFF</td> <td>#3 or #4, depending on which lamp fails</td> </tr> </tbody> </table> | Aspect nr.           | Signal vector value  |                      |                      |   |  |  | When not available, degrade to aspect nr. | 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte | 3 <sup>rd</sup> byte | 4 <sup>th</sup> byte | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte | #1 | 0x01 | 0xFF | 0xFF | 0xFF | 0xFF | 0xFF | N/A | #2 | 0x07 | 0xFF | 0x06 | 0xFF | 0xFF | 0xFF | #1 | #3 | 0x07 | 0xFF | 0x09 | 0xFF | 0xFF | 0xFF | #1 or #2, depending on which lamp fails | #4 | 0x05 | 0xFF | 0x06 | 0xFF | 0xFF | 0xFF | #2 or #1, depending on which lamp fails | #5 | 0x05 | 0xFF | 0x09 | 0xFF | 0xFF | 0xFF | #3 or #4, depending on which lamp fails |
| Aspect nr. | Signal vector value  |  |                      |                      |                      |                      | When not available, degrade to aspect nr. |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
|            | 1 <sup>st</sup> byte | 2 <sup>nd</sup> byte   | 3 <sup>rd</sup> byte | 4 <sup>th</sup> byte | 5 <sup>th</sup> byte | 6 <sup>th</sup> byte |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| #1         | 0x01                 | 0xFF   | 0xFF                 | 0xFF                 | 0xFF                 | 0xFF                 | N/A                                       |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| #2         | 0x07                 | 0xFF   | 0x06                 | 0xFF                 | 0xFF                 | 0xFF                 | #1  |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| #3         | 0x07                 | 0xFF   | 0x09                 | 0xFF                 | 0xFF                 | 0xFF                 | #1 or #2, depending on which lamp fails   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| #4         | 0x05                 | 0xFF   | 0x06                 | 0xFF                 | 0xFF                 | 0xFF                 | #2 or #1, depending on which lamp fails   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| #5         | 0x05                 | 0xFF   | 0x09                 | 0xFF                 | 0xFF                 | 0xFF                 | #3 or #4, depending on which lamp fails   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.290  | Info                 | <p>Case 1: Flashing green lamp fails &gt; yellow lamp lighted instead<br/> <math>Ks\ 1 + Sv\ 3\ (9) &gt; Ks\ 2 + Sv\ 3\ (9)</math></p> <p>Case 2: Speed indicator 9 fails &gt; speed indicator 6 lighted instead<br/> <math>Ks\ 1 + Sv\ 3\ (9) &gt; Ks\ 1 + Sv\ 3\ (6)</math></p>  |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.274  | Head                 | <b>6.1.4.2 Additional degradation information</b>  |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.275  | Info                 | In specific cases, the subsystem Electronic Interlocking can send additional degradation information to the subsystem Light Signal. This can be used when there is more than one option how to apply degradation and the preferred choice depends on which route has been set.   |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.276  | Info                 | The subsystem Electronic Interlocking will send this additional information with the commanded signal aspect, independent of the fact whether degradation needs to be applied. If degradation needs to be applied, the subsystem Light Signal will take this additional information into account without further interaction with the interlocking.  |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.277  | Head                 | <b>6.1.5 Luminosity</b>  |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.278  | Info                 | The brightness of the background of a light signal differs greatly between daylight and night conditions. To ensure optimum visibility of the signal lamps, the luminosity of the light signal is managed. During the daylight period, the signal lamps will be illuminated more brightly, to ensure they stand out against the background. During the night, the lamps are dimmed, to avoid blinding of the train driver.   |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.279  | Info                 | Depending on national specifications and local conditions, one of the two luminosities can be defined as the default luminosity of a light signal or signal group.   |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |
| Eu.DK.173  | Head                 | <b>6.2 Points</b>  |                      |                      |                      |                      |   |  |  |   |                      |                      |                      |                      |                      |                      |    |      |      |      |      |      |      |     |    |      |      |      |      |      |      |    |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |    |      |      |      |      |      |      |   |

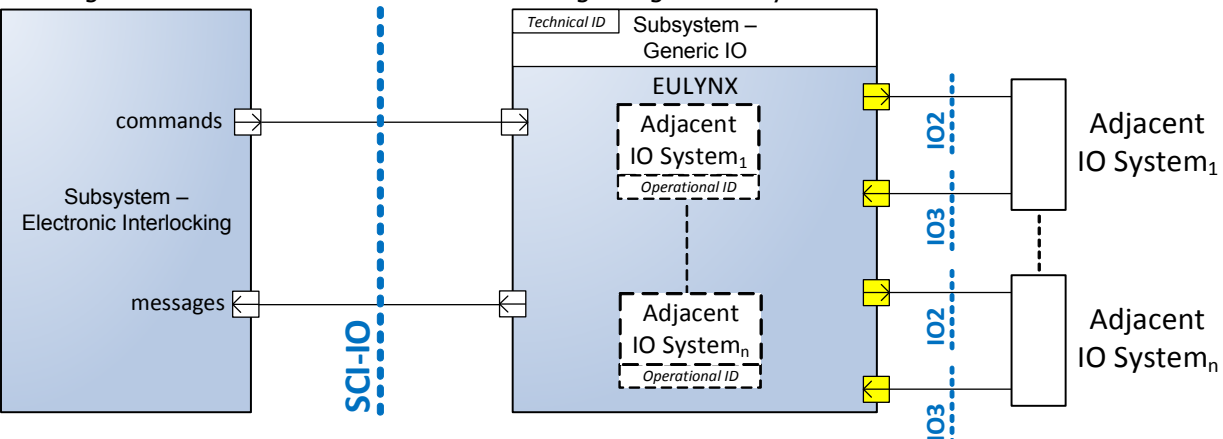
| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.187 | Info | Moveable elements, whose position may be changed by a point machine, are integrated to the interlocking system through the subsystem Point.  |
| Eu.DK.219 | Info | The subsystem Point controls one point as a single operational element.  |
| Eu.DK.188 | Info | The subsystem Point is used to control and monitor the point machines of the following elements: <ul style="list-style-type: none"> <li>• simple points</li> <li>• double slip points (as two operational elements)</li> <li>• single slip points (as two operational elements)</li> <li>• moveable switch diamond crossings</li> <li>• moveable crossing noses on any of the above (as part of the operational element)</li> <li>• derailleurs</li> </ul> |
| Eu.DK.468 | Info | A point machine has 2 functionalities: <ol style="list-style-type: none"> <li>a. Moving the point</li> <li>b. Detecting the point position</li> </ol>  |
| Eu.DK.469 | Info | There are two possible configurations: <ul style="list-style-type: none"> <li>- 'Point detector': A point machine with only functionality b.</li> <li>- 'Full functionality': A point machine with functionality a and b.</li> </ul>   |
| Eu.DK.220 | Info | A point can be equipped with one or more point machines. In case of more than one point machine, it is possible that some point machines only function as a point detector, without moving the point blades.   |
| Eu.DK.221 | Info | EULYNX specifies the functional interface to the point machine. The physical interface to the point machine is covered by national specifications.   |
| Eu.DK.222 | Info | The diagram below shows the main definitions regarding the subsystem Point. <div style="text-align: center;">  <p style="border: 1px solid black; padding: 5px; display: inline-block; margin: 10px auto;">Note: Point machines 2 to n can also be implemented as point detectors only</p> </div>   |
| Eu.DK.470 | Info | There are 2 implementation variants of the functional interface to the point machine: <ul style="list-style-type: none"> <li>- non-4-wire</li> <li>- 4-wire</li> </ul>   |
| Eu.DK.471 | Info | For the non-4-wire implementation, EULYNX only defines functional input and output information.  |
| Eu.DK.472 | Info | For the 4-wire implementation, the input information is represented as 4-wire patterns.  |
| Eu.DK.473 | Info | The 4-wire pattern consists of four digits, each being in a state of „1" or „0". The state of „1" represents a closed contact in the 4-wire circuit while „0" represents an open contact in the 4-wire circuit.  |
| Eu.DK.474 | Info | There are four contact pairs, where each pair is represented by a specific digit in the 4-wire pattern (ABCD): <ul style="list-style-type: none"> <li>Contact 1+3 -&gt; Digit A</li> <li>Contact 1+4 -&gt; Digit B</li> <li>Contact 2+4 -&gt; Digit C</li> <li>Contact 2+3 -&gt; Digit D</li> </ul>  |

| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.476 | Info | <p>The figure shows a schematic representation of the 4-wire circuit</p>    |
| Eu.DK.499 | Head | <b>6.2.1 Point machine position</b>   |
| Eu.DK.500 | Info | The subsystem Point interprets the signal at the point machine or point detector interface, corresponding to the physical position of the moveable component. As a simplification, this is expressed as the point machine or point detector 'detecting' the position of the moveable component.   |
| Eu.DK.501 | Info | <p><b>End position (left or right)</b><br/>The point machine reliably detects that the moveable component is either the left or right position.</p>   |
| Eu.DK.502 | Info | <p><b>No end position</b><br/>If implemented as a 4-wire interface, the point machine is not able to detect neither end position nor an unintended position of the moveable component.<br/>If implemented as a non-4-wire interface, the point machine is able to detect that the moveable component is not in either end position.</p>   |
| Eu.DK.503 | Info | <p><b>Unintended position</b><br/>If implemented as a 4-wire interface, the point machine is able to reliably detect that the moveable component is in a position that does not correspond to the commanded end position. This detection of an 'unintended position' may be caused by a trailing movement or occur for other reasons.<br/>If implemented as a non-4-wire interface, the point machines may not be equipped with the functionality to detect an 'unintended position'.</p> |
| Eu.DK.504 | Head | <b>6.2.2 Overall point position</b>   |
| Eu.DK.505 | Info | When a moveable element is equipped with more than one point machine interface to the subsystem Point (some of them may be only point detectors), their inputs must be combined and consolidated into an overall point position that is reported to the interlocking.   |
| Eu.DK.507 | Info | <p><b>End position (left or right)</b><br/>This overall position is reported to the interlocking only when all configured point machine detect the corresponding end position.</p>  |
| Eu.DK.508 | Info | <p><b>Unintended position</b><br/>This overall position is reported to the interlocking as soon as one point machine detects an unintended position.</p>  |
| Eu.DK.509 | Info | <p><b>No end position</b><br/>This overall position is reported to the interlocking whenever the detected inputs from the configured point machines don't correspond to an end position or to an unintended position.</p>   |
| Eu.DK.506 | Info | The functionality of the subsystem Point does not contain any 'memory' of the reported state. As soon as the conditions are fulfilled to report a different state, the new state is reported to the EIL.  |
| Eu.DK.477 | Head | <b>6.2.3 Degraded point position</b>  |
| Eu.DK.475 | Info | When a moveable element is equipped with more than one point machine interfaces to the subsystem Point (some of them may be only point detectors), more elaborate information about the overall position of the element is available in the subsystem. Some of this information can be useful for the interlocking system to increase availability of the infrastructure.   |

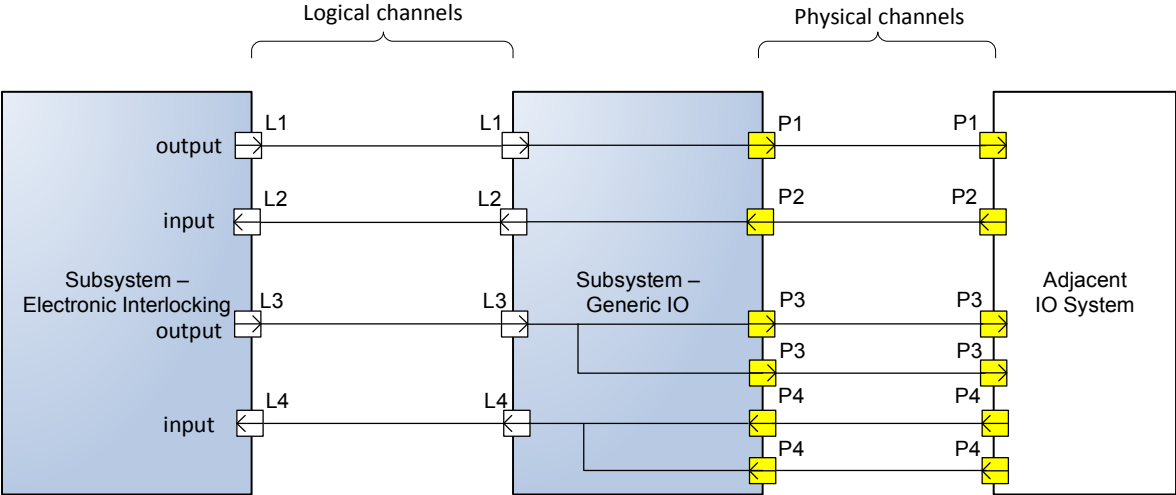
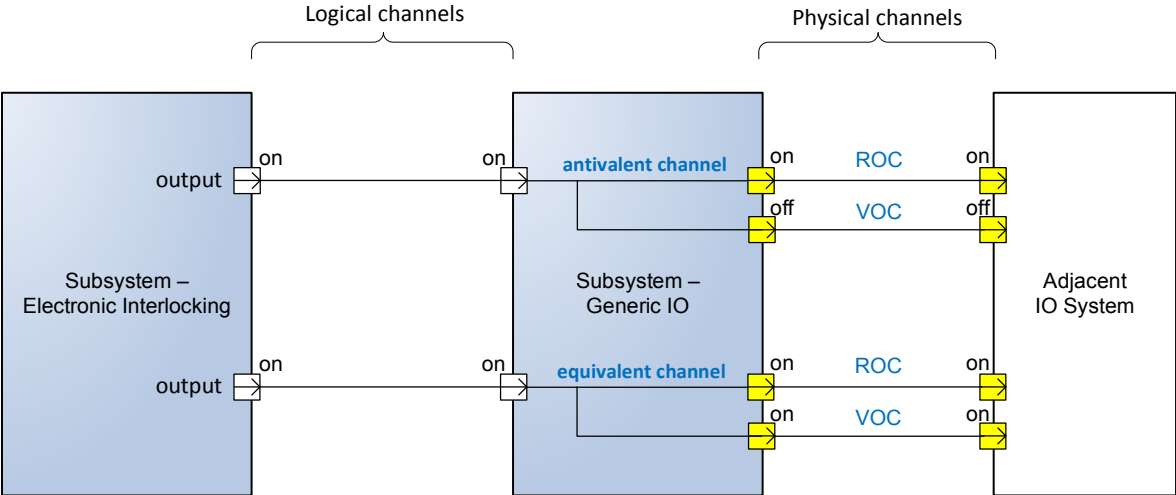
| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.478 | Info | In certain 'degraded' states, the point position may be deemed reliable enough to provide flank protection to other routes. It is not reliable enough to drive over the point with normal speed. The use of the 'degraded' position depends on national implementation in the interlocking logic.   |
| Eu.DK.479 | Info | For this purpose, two levels of reliability are defined for detected point positions.   |
| Eu.DK.480 | Info | <b>End position (left or right)</b><br>The moveable element can be used to satisfy any operational need. E.g., points in route body and overlap or flank protection.  |
| Eu.DK.481 | Info | <b>Degraded position (left or right)</b><br>The moveable element can only be used for specific operational needs. E.g., only limited flank protection can be accepted.  |
| Eu.DK.482 | Info | To determine the level of reliability of the detected overall position, each point machine must be configured as 'crucial' or as 'non-crucial', depending on how crucial the position detected by that point machine is to determine the overall position. At least one point machine must be configured as 'crucial'.  |
| Eu.DK.483 | Info | To be able to report an <b>end position</b> to the interlocking system, all point machines, whether they are configured as 'crucial' or 'non-crucial', must be detecting the same end position.   |
| Eu.DK.484 | Info | To be able to report a <b>degraded position</b> to the interlocking system, all point machines, which are configured as "crucial" must be detecting the same end position. The point machines configured as "non-crucial" don't need to detect the same end position, as long as they don't detect the opposite end position.   |
| Eu.DK.485 | Info | If there is no need to report degraded positions to the interlocking system, e.g. because the interlocking logic doesn't use this information, all point machines can be configured as "crucial".   |
| Eu.DK.486 | Head | <b>6.2.4 Crank handle operation</b>   |
| Eu.DK.189 | Info | Crank handle operation is used to operate a point machine by hand. For staff safety reasons, point machines are isolated from the power at the point machine when a crank handle is in use.   |
| Eu.DK.510 | Head | <b>6.2.5 Trailing evaluation in the interlocking</b>  |
| Eu.DK.511 | Info | The required functionality related to detecting trailing on the functional level of the interlocking depends heavily on national signalling regulations and practices, which in their place depend on national operational rules.   |
| Eu.DK.512 | Info | On this functional level, position information from the individual point may be combined with other information available in the EIL, e.g. occupancy of TVP sections or route statuses, to conclude that a point is in a state that must be considered 'trailed'.   |
| Eu.DK.513 | Info | The conclusion that a certain point must be considered 'trailed' may also be based only on position information from the point object controller. For this, it is needed that the subsystem point reports 4 states, so it is possible to distinguish a detected 'unintended position' from a loss of position detection.  |
| Eu.DK.514 | Info | The conditions to no longer consider a certain point as 'trailed' again depend on national operational and signalling rules.  |
| Eu.DK.206 | Head | <b>6.3 Train detection systems</b>  |
| Eu.DK.223 | Info | Track vacancy proving (TVP) and Train detection point (TDP) functions are integrated to the interlocking system through the subsystem Train Detection System (TDS).<br>Track vacancy proving may be implemented with track circuits or axle counting systems. Train detection points may be implemented with the same wheel sensors used for track vacancy proving using an axle counter system or with separate wheel sensors.<br>One subsystem Train Detection System may control one or many TVP sections and TDP locations. |
| Eu.DK.224 | Head | <b>6.3.1 TVP Sections</b>   |
| Eu.DK.225 | Info | Track vacancy proving is the function that proves that a defined section of track is vacant. For this purpose, the track is divided into distinct portions, or TVP sections (TVPS). One subsystem Train Detection System may control more than one TVP section.   |
| Eu.DK.489 | Head | <b>6.3.2 TDP locations</b>  |
| Eu.DK.490 | Info | The Train detection point function proves that a train passes a defined track location. For this purpose, a wheel detection sensor is located at a position on the track to identify the passing of train wheels in a certain direction.  |
| Eu.DK.226 | Head | <b>6.3.3 Technical and operational identifiers</b>  |
| Eu.DK.227 | Info | The subsystem TDS has a technical identifier. In telegrams that are exchanged between the subsystem TDS and the subsystem Electronic Interlocking and relate to the generic behaviour of the subsystem TDS, the technical identifier of the subsystem TDS is used as identifier of the sender or receiver respectively.   |
| Eu.DK.228 | Info | Every TVPS that is controlled by a subsystem TDS has an operational identifier. In telegrams that are exchanged between the subsystem TDS and the subsystem Electronic Interlocking and regard the specific behaviour of individual TVP sections, the operational identifier of the TVPS is used as identifier of the sender or receiver respectively.  |
| Eu.DK.491 | Info | Every TDP that is controlled by a subsystem TDS has an operational identifier. In telegrams that are exchanged between the subsystem TDS and the subsystem Electronic Interlocking and concern the specific behaviour of individual TDP locations, the operational identifier of the TDP is used as identifier of the sender or receiver respectively.  |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.229 | Head | <b>6.3.4 Types of track vacancy proving</b>  |
| Eu.DK.230 | Head | <b>6.3.4.1 Axle counters</b>   |
| Eu.DK.231 | Info | In an axle counter system, TVP sections are logical entities consisting of a section of track that is usually closed off by at least two detection points. One detection point can function as entry/exit point of more than one TVP section. On dead end tracks, one detection point can function as the sole entry/exit point of one TVP section.  |
| Eu.DK.232 | Info | In an axle counter system, one instance of the subsystem TDS usually covers several TVP sections.  |
| Eu.DK.238 | Info | <p>The diagram below shows the main definitions regarding the subsystem TDS when implemented with axle counters.</p>  <p>The diagram, titled 'TDS with axle counters', illustrates the functional interface. On the left, a box represents the 'Subsystem – Electronic Interlocking', which sends 'commands' and receives 'messages' from the 'Subsystem – Train Detection System'. A vertical dashed blue line labeled 'SCI-TDS' separates the two. The 'Subsystem – Train Detection System' contains the 'EULYNX' layer, which manages multiple 'TVPS' (Train Vacancy Proving) sections, labeled 'TVPS<sub>1</sub>' and 'TVPS<sub>n</sub>', each with its own 'Operational ID'. These TVPS sections are connected to 'Wheel' sensors on the track via 'TDS2' interfaces.</p> |
| Eu.DK.233 | Head | <b>6.3.4.2 Track circuits</b>  |
| Eu.DK.234 | Info | In a track circuit system, a TVP sections is a logical entity that usually coincides with the physical entity of one track circuit section. One logical TVP section can be composed of several track circuit sections.   |
| Eu.DK.235 | Info | In a track circuit system, one instance of the subsystem TDS covers one or several TVP sections.   |
| Eu.DK.236 | Info | EULYNX specifies the functional interface to the track circuits. The physical interface to the track circuit relays and possibly power off monitoring is covered by national specifications.   |
| Eu.DK.240 | Info | <p>The diagram below shows the main definitions regarding the subsystem TDS when implemented with track circuits.</p>  <p>The diagram, titled 'TDS with track circuits', shows a similar interface to the axle counter system. It features the 'Subsystem – Electronic Interlocking' and the 'Subsystem – Train Detection System' separated by the 'SCI-TDS' boundary. The 'EULYNX' layer manages 'TVPS' sections ('TVPS<sub>1</sub>' and 'TVPS<sub>n</sub>') with 'Operational ID'. These sections interface with 'National specifications' through 'track circuit relay' and 'power off monitoring' components. The physical connection to the 'Wheel' sensors is also shown via 'TDS2' interfaces.</p>  |
| Eu.DK.492 | Head | <b>6.3.4.3 Train detection points</b>  |
| Eu.DK.493 | Info | In a train detection system, a TDP location is a logical entity that coincides with the physical entity of one detection point.  |
| Eu.DK.494 | Info | In a train detection system, one instance of the subsystem TDS usually covers several TDP locations.   |
| Eu.DK.495 | Info | EULYNX specifies the functional interface to the detection point. The physical interface to the implementation of the detection point is covered by national specifications.   |

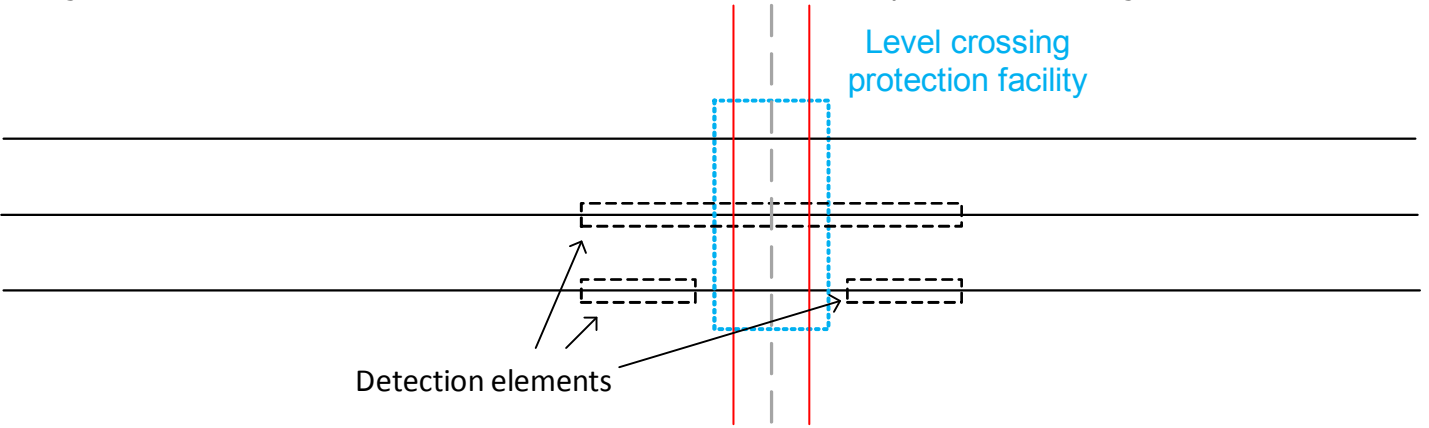
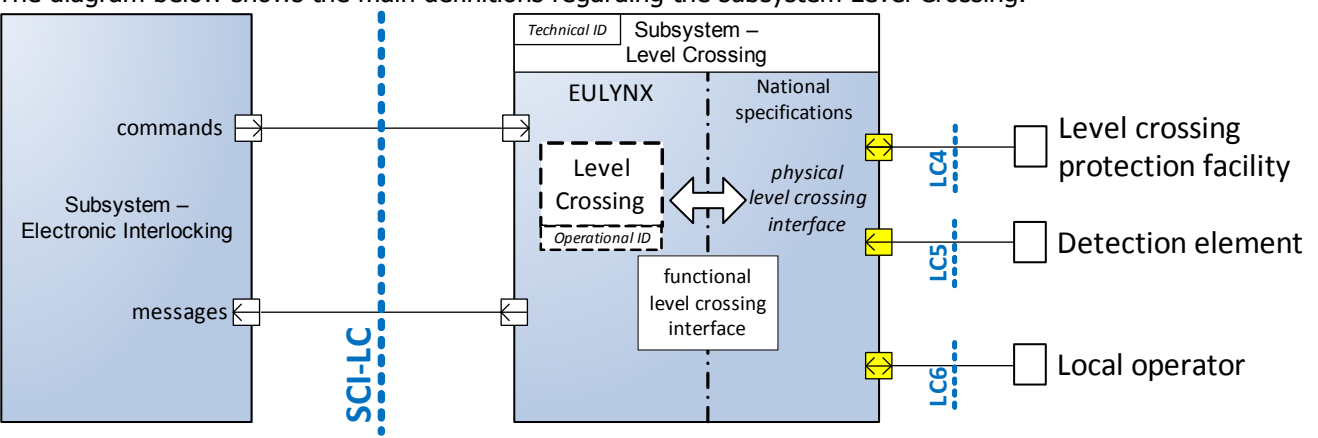
| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.497 | Info | <p>The diagram below shows the main definitions regarding the subsystem TDS when implemented with train detection points.</p> <div data-bbox="371 178 1973 934" style="border: 1px solid black; padding: 10px;"> <p style="text-align: center; border: 1px solid black; display: inline-block; padding: 5px;">TDS with train detection points</p> <p>The diagram illustrates the architecture of the TDS with train detection points. It shows two main subsystems: 'Subsystem - Electronic Interlocking' and 'Subsystem - Train Detection System (EULYNX)'. The EULYNX subsystem contains multiple 'Operational ID' blocks for 'TDP<sub>1</sub>' and 'TDP<sub>n</sub>', which are connected to 'Wheels' via 'TDS2' interfaces. The 'Subsystem - Electronic Interlocking' sends 'messages' to the EULYNX system. A vertical dashed line labeled 'SCI-TDS' separates the two subsystems. The 'Wheels' are connected to the EULYNX system via 'TDS2' interfaces, which are also connected to the 'Wheels'.</p> </div> |
| Eu.DK.115 | Head | <b>6.4 IO elements and systems</b>  |
| Eu.DK.127 | Info | <p>Individual signalling components are integrated to the interlocking system through the subsystem Generic IO. The subsystem Generic IO should not be used for interfacing with components or subsystems defined through dedicated SCI interfaces as SCI-P or SCI-LS. The controlling and monitoring of these components is performed with generic inputs and outputs, configurable for each specific application.</p>   |
| Eu.DK.128 | Head | <b>6.4.1 Functional elements</b>  |
| Eu.DK.129 | Info | <p>The IO elements or systems are referred to as "Adjacent IO Systems" and may be grouped according to their functionality:</p> <ul style="list-style-type: none"> <li>• elements requiring releasing and locking functionality</li> <li>• elements used as indicators, but not controlled as a light signal</li> <li>• elements used for detection</li> <li>• elements serving as local control panels</li> </ul>  |
| Eu.DK.130 | Info | <p>Functional elements used as lockable devices, requiring releasing and locking functionality, may be any of the following:</p> <ul style="list-style-type: none"> <li>• Moveable bridges</li> <li>• Tunnel gates / track closing gates</li> <li>• Key lock</li> <li>• Key lock on the line</li> <li>• Catenary elevating system</li> </ul>  |
| Eu.DK.131 | Info | <p>Functional elements used as indicators may be any of the following:</p> <ul style="list-style-type: none"> <li>• Warning lamp</li> <li>• Fouling point control lamp</li> <li>• Derailment and tracking indicator</li> </ul>  |
| Eu.DK.132 | Info | <p>Functional elements used as detectors may be any of the following:</p> <ul style="list-style-type: none"> <li>• Avalanche detection</li> </ul>   |

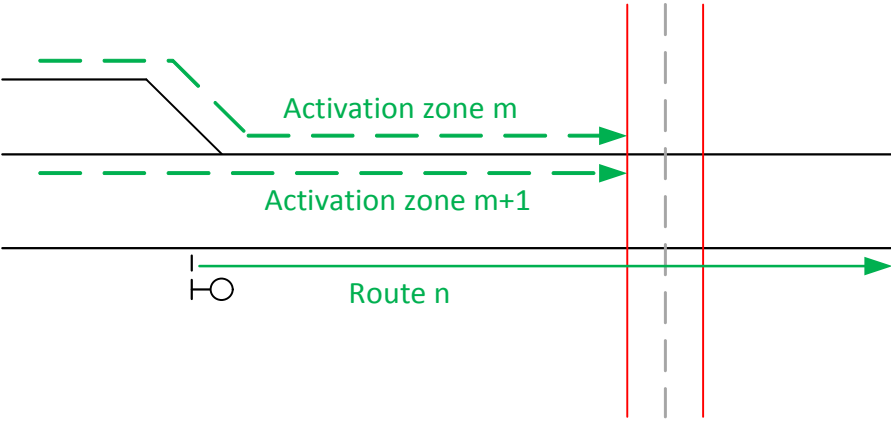
| ID        | Type | Domain knowledge   |
|-----------|------|--|
|           |      | <ul style="list-style-type: none"> <li>• Hot wheel box detector</li> <li>• Flat wheel detector</li> <li>• Gas detector</li> <li>• Fire detectors</li> <li>• Door sensors</li> <li>• Intrusion detector</li> <li>• Overload detector</li> <li>• Light intensity detection</li> <li>• Trip wire detection</li> <li>• Overheating / freezing detection</li> <li>• Power supply status detection</li> </ul>  |
| Eu.DK.133 | Info | Functional elements serving as local control panels may be any of the following: <ul style="list-style-type: none"> <li>• Local control panel for single element - moveable bridge</li> <li>• Local control panel for single element - key locks</li> <li>• Local control panel for single element - derailer</li> <li>• Local control panel for single element - point</li> <li>• Local control panel for single element - catenary elevating system</li> <li>• Local control panel for single element - gates</li> <li>• Local control panel for - handling of transfer to verbal line block</li> <li>• Local control panel for areas (multiple elements)</li> </ul> |
| Eu.DK.135 | Info | The above lists are non-exclusive.   |
| Eu.DK.136 | Head | <b>6.4.2 Generic IO definition</b>   |
| Eu.DK.241 | Info | One subsystem Generic IO may control more than one Adjacent IO system. The Adjacent IO systems can be homogeneous or heterogeneous.  |
| Eu.DK.242 | Info | One Adjacent IO System may need to be controlled by more than one subsystem Generic IO, for example in case of a many-button local control panel (where the number of buttons exceeds the amount of channels available on one subsystem).  |
| Eu.DK.243 | Head | <b>6.4.2.1 Technical and operational identifiers</b>   |
| Eu.DK.244 | Info | The subsystem Generic IO has a technical identifier. In telegrams that are exchanged between the subsystem Generic IO and the subsystem Electronic Interlocking and relate to the generic behaviour of the subsystem Generic IO, the technical identifier of the subsystem Generic IO is used as identifier of the sender or receiver respectively.  |
| Eu.DK.245 | Info | Every Adjacent IO System that is controlled by a subsystem Generic IO has an operational identifier. In telegrams that are exchanged between the subsystem Generic IO and the subsystem Electronic Interlocking and regard the specific behaviour of individual Adjacent IO Systems, the operational identifier of the Adjacent IO System is used as identifier of the sender or receiver respectively.  |
| Eu.DK.246 | Info | The diagram below shows the main definitions regarding the subsystem Generic IO.   |



| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.139 | Info | <p>A logical channel represents a channel between the subsystem Electronic Interlocking and the subsystem Generic IO.</p> <p>A logical channel may be configured as:</p> <ul style="list-style-type: none"> <li>input, representing the information, which is available to subsystem Electronic Interlocking</li> <li>output, representing a command, which is sent from subsystem Electronic Interlocking</li> </ul> <p>A logical channel may be implemented as:</p> <ul style="list-style-type: none"> <li>single channel, when assigned to one physical channel</li> <li>antivalent channel, when assigned to two physical channels evaluated as antivalent</li> <li>equivalent channel, when assigned to two physical channels evaluated as equivalent</li> </ul> |
| Eu.DK.247 | Info | <p>Several logical channels can be addressed to the same Adjacent IO System. The logical channels can be of the same type or of differing types.</p>  |
| Eu.DK.137 | Info | <p>The following diagram displays the terminology of logical and physical channels for connection of an Adjacent IO Systems to the interlocking system through the subsystem Generic IO:</p>  |
| Eu.DK.158 | Info | <p>Channel definition</p>  <p>The diagram illustrates the connection between three subsystems: Subsystem - Electronic Interlocking, Subsystem - Generic IO, and Adjacent IO System. Logical channels (L1-L4) are shown on the left, connecting to physical channels (P1-P4) in the middle, which then connect to the Adjacent IO System on the right. L1 and L2 are outputs, while L3 and L4 are inputs. P1 and P2 are outputs, while P3 and P4 are inputs.</p>  |
| Eu.DK.140 | Info | <p>Antivalent and equivalent configurations are displayed on the following diagram:</p>   |
| Eu.DK.159 | Info | <p>Example of antivalent and equivalent configurations</p>  <p>The diagram illustrates two configurations: antivalent and equivalent. In the antivalent configuration, a logical output channel connects to two physical channels (ROC and VOC) in the Subsystem - Generic IO, which then connect to two physical channels (ROC and VOC) in the Adjacent IO System. In the equivalent configuration, a logical output channel connects to two physical channels (ROC and VOC) in the Subsystem - Generic IO, which then connect to two physical channels (ROC and VOC) in the Adjacent IO System.</p>   |
| Eu.DK.142 | Info | <p>A logical output channel may be configured as:</p> <ul style="list-style-type: none"> <li>monitored, if the subsystem Generic IO proves internally that the outputs are set to the intended value (This monitoring only serves to report the technical failure of the output channel. If for a specific application a fail-safe supervision is required, an input channel shall be used for confirming the activation of the output)</li> <li>not monitored</li> </ul>   |

| ID                                   | Type             | Domain knowledge  |                                      |                                 |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|--------------------------------------|------------------|---|--------------------------------------|---------------------------------|------------------|----------------------------------|---------------------------------|------------|---|---|-----------|---------|---|---|---|-------|---|---|---|-------|---|---|-----------|---------|------------|---|---|---|-------|---|---|-----------|---------|---|---|-----------|---------|---|---|---|-------|--------|---|--------------|---|-------|---|--------------|---|-------|
| Eu.DK.143                            | Info             | <p>A logical channel may be in one of the following states:</p> <ul style="list-style-type: none"> <li>switched on</li> <li>switched off</li> <li>flashing (only output)</li> <li>disturbed (operationally, when the anti/equivalence condition is not fulfilled, or technically)</li> </ul>  |                                      |                                 |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| Eu.DK.144                            | Info             | <p>A physical channel represents a channel between the subsystem Generic IO and the Adjacent IO System.</p> <p>A physical channel may be configured as:</p> <ul style="list-style-type: none"> <li>input, representing the information available to the subsystem Generic IO;</li> <li>output, representing the information available from the subsystem Generic IO;</li> </ul>   |                                      |                                 |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| Eu.DK.145                            | Info             | <p>A physical channel is referred to as the following:</p> <ul style="list-style-type: none"> <li><b>Reference Output Channel (ROC):</b><br/>The reference output channel is a physical output channel. It is configured to be antivalent, equivalent or single channel. The reference output channel is used to represent the information of the logical output channel. The logical output channel is commanded by the subsystem Electronic Interlocking via SCI-IO.</li> <li><b>Validation Output Channel (VOC):</b><br/>The validation output channel is a physical output channel. It is always implemented in pair with a reference output channel, and is configured identically as the reference output channel. <u>The validation output channel is not used for single channels.</u><br/>The state of the validation output channel is switched by the subsystem Generic IO internally, in an antivalent or equivalent way to the reference output channel.</li> <li><b>Reference Input Channel (RIC):</b><br/>The reference input channel is a physical input channel. It is configured to be antivalent, equivalent or single channel.<br/>The reference input channel is used for providing the information for the logical input channel. If no disturbance is detected, the logical input channel is reported to the subsystem Electronic Interlocking via SCI-IO.</li> <li><b>Validation Input Channel (VIC):</b><br/>The validation input channel is a physical input channel. It is always implemented in pair with a reference input channel, and is configured identically as the reference input channel. <u>The validation input channel is not used for single channels.</u><br/>The state of validation input channel is used by the subsystem Generic IO internally for proving the condition to the reference input channel.</li> </ul> |                                      |                                 |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| Eu.DK.204                            | Info             | <p>The relation between physical and logical channels</p> <table border="1" data-bbox="368 1213 1427 1665"> <thead> <tr> <th>Physical channels are configured as:</th> <th>Value of RIC/ROC</th> <th>Value of VIC/VOC</th> <th>Value of related logical channel</th> <th>Evaluation of physical channels</th> </tr> </thead> <tbody> <tr> <td rowspan="4">antivalent</td> <td>0</td> <td>0</td> <td>Disturbed</td> <td>Invalid</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>Valid</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>Valid</td> </tr> <tr> <td>1</td> <td>1</td> <td>Disturbed</td> <td>Invalid</td> </tr> <tr> <td rowspan="4">equivalent</td> <td>0</td> <td>0</td> <td>0</td> <td>Valid</td> </tr> <tr> <td>0</td> <td>1</td> <td>Disturbed</td> <td>Invalid</td> </tr> <tr> <td>1</td> <td>0</td> <td>Disturbed</td> <td>Invalid</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>Valid</td> </tr> <tr> <td rowspan="2">single</td> <td>0</td> <td>Not existent</td> <td>0</td> <td>Valid</td> </tr> <tr> <td>1</td> <td>Not existent</td> <td>1</td> <td>Valid</td> </tr> </tbody> </table>  | Physical channels are configured as: | Value of RIC/ROC                | Value of VIC/VOC | Value of related logical channel | Evaluation of physical channels | antivalent | 0 | 0 | Disturbed | Invalid | 0 | 1 | 0 | Valid | 1 | 0 | 1 | Valid | 1 | 1 | Disturbed | Invalid | equivalent | 0 | 0 | 0 | Valid | 0 | 1 | Disturbed | Invalid | 1 | 0 | Disturbed | Invalid | 1 | 1 | 1 | Valid | single | 0 | Not existent | 0 | Valid | 1 | Not existent | 1 | Valid |
| Physical channels are configured as: | Value of RIC/ROC | Value of VIC/VOC  | Value of related logical channel     | Evaluation of physical channels |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| antivalent                           | 0                | 0   | Disturbed                            | Invalid                         |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|                                      | 0                | 1   | 0                                    | Valid                           |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|                                      | 1                | 0   | 1                                    | Valid                           |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|                                      | 1                | 1   | Disturbed                            | Invalid                         |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| equivalent                           | 0                | 0   | 0                                    | Valid                           |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|                                      | 0                | 1   | Disturbed                            | Invalid                         |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|                                      | 1                | 0   | Disturbed                            | Invalid                         |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|                                      | 1                | 1   | 1                                    | Valid                           |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| single                               | 0                | Not existent  | 0                                    | Valid                           |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
|                                      | 1                | Not existent  | 1                                    | Valid                           |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| Eu.DK.351                            | Info             | <p>The subsystem Electronic Interlocking has no knowledge whether a logical channel is implemented with a single, antivalent or equivalent physical channels.</p>   |                                      |                                 |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| Eu.DK.146                            | Head             | <p><b>6.4.3 Application library</b></p>   |                                      |                                 |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |
| Eu.DK.147                            | Info             | <p>The subsystem Generic IO has no functional knowledge about the attached Adjacent IO Systems, except grouping the channels connected to each Adjacent IO System. The functional knowledge about the Adjacent IO Systems is in the subsystem Electronic Interlocking. An application library may be used by IMs to describe the individual logical and physical implementation of each Adjacent IO System (such as key lock, moveable bridge...).</p>  |                                      |                                 |                  |                                  |                                 |            |   |   |           |         |   |   |   |       |   |   |   |       |   |   |           |         |            |   |   |   |       |   |   |           |         |   |   |           |         |   |   |   |       |        |   |              |   |       |   |              |   |       |

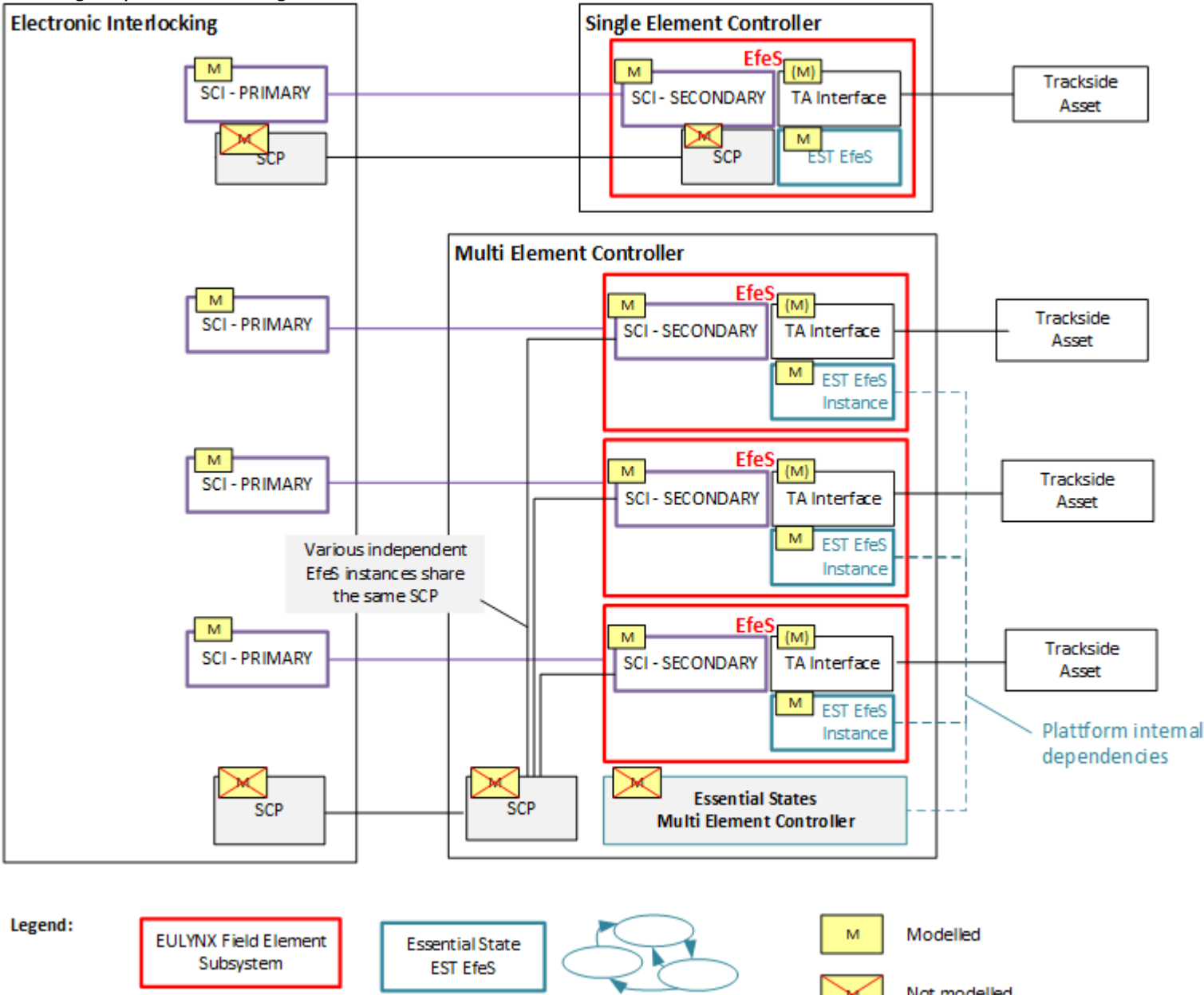
| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.148 | Head | <b>6.4.4 Constraints with application of subsystem Generic IO</b>   |
| Eu.DK.149 | Info | The use of the subsystem Generic IO is limited to a realistic sampling rate of 1Hz.   |
| Eu.DK.150 | Info | The mitigation of bouncing effects on the input channels is not a function of the application layer, this must be handled by the physical implementation.   |
| Eu.DK.151 | Info | The following issues must be considered by the physical implementation: <ul style="list-style-type: none"> <li>• debouncing of the inputs</li> <li>• detection of fleeting inputs shorter than the available sampling rate.</li> </ul>  |
| Eu.DK.61  | Head | <b>6.5 Level Crossing</b>   |
| Eu.DK.291 | Info | Systems to prevent collisions between trains and road users at level crossings are integrated to the interlocking system through the subsystem Level Crossing or through the adjacent system External Level Crossing System.  |
| Eu.DK.433 | Info | The subsystem Level Crossing is used to integrate level crossing systems for which the activation and deactivation logic is handled externally of the subsystem (for example in the interlocking system or in the Radio Block Centre)   |
| Eu.DK.292 | Info | The subsystem Level Crossing controls one level crossing as a single operational element.   |
| Eu.DK.293 | Info | The level crossing protection facility controls all protection devices that are used to warn and obstruct road traffic. It may contain: <ul style="list-style-type: none"> <li>• Road signals (with warning lamps and/or warning bells)</li> <li>• Barriers</li> <li>• Obstacle detector</li> <li>• Warning signs</li> <li>• Other devices</li> </ul> |
| Eu.DK.294 | Info | The level crossing protection facility protects the area where road traffic (including motor vehicles, bicycles, pedestrians, etc.) is at risk of being hit by a passing train, called the level crossing protection area.  |
| Eu.DK.348 | Info | <p>The figure below shows the main definitions of elements related to the subsystem Level Crossing.</p>   |
| Eu.DK.295 | Info | EULYNX specifies the functional interface to the level crossing. The physical interface to the level crossing protection facility is covered by national specifications.  |
| Eu.DK.347 | Info | <p>The diagram below shows the main definitions regarding the subsystem Level Crossing.</p>   |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.498 | Info | The Subsystem - Level Crossing does not control (de)activation points. Different track element may act as (de)activation point, depending on the activation logic, which may be handled in the interlocking or RBC. This includes detection elements of the Subsystem - Level Crossing, TVP sections, Train detection points or train position reports.  |
| Eu.DK.299 | Head | <b>6.5.1 Functions of the subsystem Level Crossing</b>   |
| Eu.DK.301 | Head | <b>6.5.1.1 Activation and deactivation</b>   |
| Eu.DK.302 | Info | The activation (or deactivation) of the level crossing is directly triggered by a command from the interlocking. That means that the complete level crossing protection facility shall be activated (or deactivated) without an evaluation of conditions on track, direction or route by the level crossing.   |
| Eu.DK.420 | Info | Activation or deactivation may be commanded based on one or more conditions in the interlocking. Examples of conditions leading to an activation are: <ul style="list-style-type: none"> <li>• route (or overlap) setting resulting in a request to activate or deactivate a level crossing</li> <li>• presence of a train in an activation zone</li> <li>• a request resulting from a command by the signaller</li> <li>• a request resulting from a command by the Radio Block Centre</li> </ul>   |
| Eu.DK.422 | Info | The figure below shows the main definitions related to conditions in the interlocking for (de)activation.   |
| Eu.DK.305 | Head | <b>6.5.1.2 Pre-activation</b>  |
| Eu.DK.306 | Info | The interlocking commands the pre-activation of a level crossing. Pre-activation is used for pre-warning traffic lights, interrupts to control units of traffic lights etc.. Interlocking conditions for pre-activation are used as for regular activation, with the difference that a pre-activation usually start some distance in rear of the start of the corresponding activation zone.   |
| Eu.DK.307 | Info | The interlocking can revoke a pre-activation by commanding a deactivation in case a previously expected train is no longer expected to activate the level crossing (e.g. it stopped in the pre-activation zone and will not continue).   |
| Eu.DK.462 | Head | <b>6.5.1.3 Activation and deactivation by local request</b>  |
| Eu.DK.328 | Info | A local operator can request activation or deactivation of the level crossing via a local operator interface. Requests can be applied for the complete or partial (e.g. one out of two tracks) level crossing protection area, based on an index. Requests are sent to the interlocking, which will evaluate relevant conditions. After evaluation, the interlocking can send activation or deactivation commands to the subsystem Level Crossing.   |
| Eu.DK.435 | Info | The level crossing protection facility may be operated independent of the interlocking system or subsystem Level Crossing (e.g. no connection to the interlocking, subsystem Level Crossing not operational). This direct operation of the level crossing protection facility and the related operational procedures are outside of the scope of EULYNX and are subject to national specifications.  |
| Eu.DK.327 | Head | <b>6.5.1.4 Local operation handover</b>  |
| Eu.DK.329 | Info | The interlocking logic may handle a handover of responsibility of the level crossing protection area to a local operator, according to national operational procedures. For this handover, commands and messages are exchanged between the interlocking and the local operation interface connected to the subsystem Level Crossing. The handover can be applied for the complete or partial (e.g. one out of two tracks) level crossing protection area, based on an index.   |
| Eu.DK.423 | Head | <b>6.5.1.5 Isolation</b>   |
| Eu.DK.421 | Info | The interlocking can command the subsystem Level Crossing to become isolated, and not react on failure of the communication. This may be used in case of engineering works on an interlocking, in order to prevent all level crossings connected to that particular interlocking to go into a fail-safe state due to failure of communication, resulting in a protected level crossing protections facility. The interlocking system guarantees and monitors the safe application of this function. Applying the isolated mode may only be permitted if for example there are no routes locked and the route setting is blocked. |
| Eu.DK.342 | Head | <b>6.5.2 Statuses</b>  |

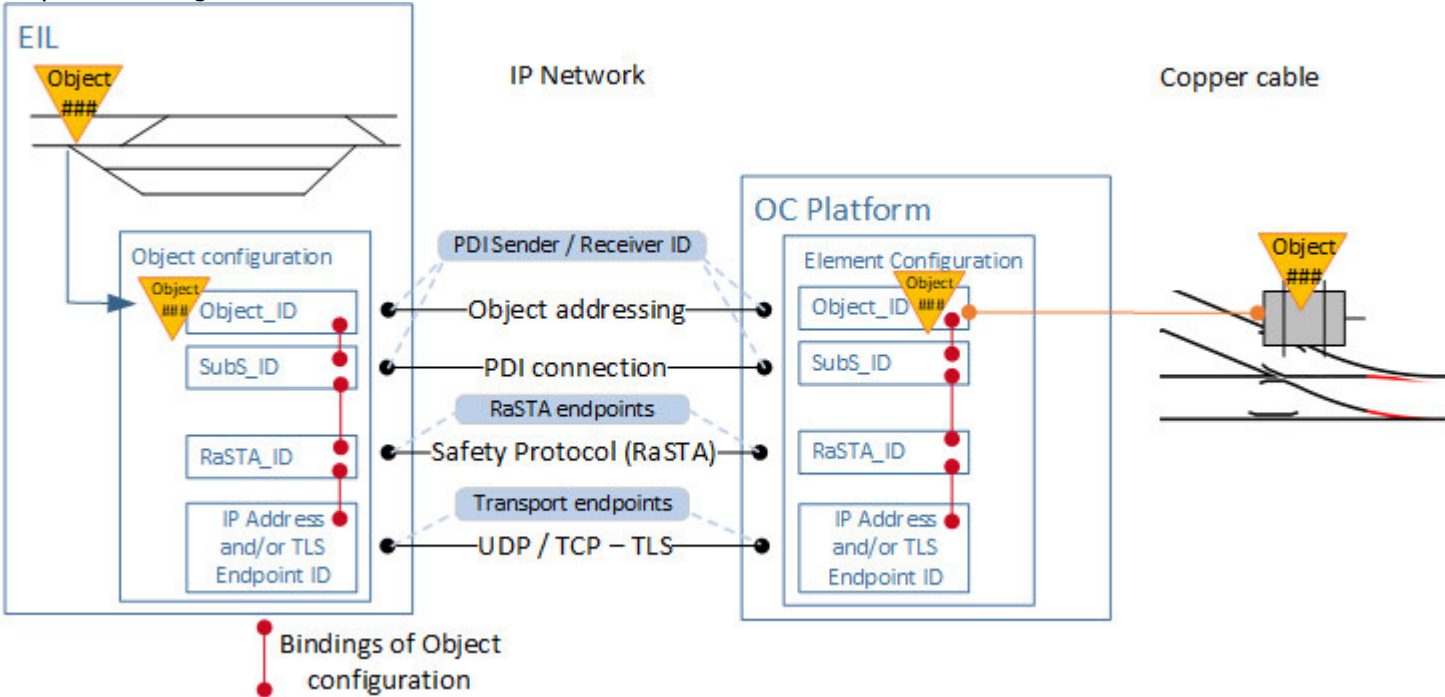
| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.343 | Info | The subsystem Level Crossing informs the interlocking of its status, based on different principles:   |
| Eu.DK.344 | Info | <b>Functional status</b><br>This message is used for the statuses of the subsystem Level Crossing which are required within the interlocking logic.   |
| Eu.DK.345 | Info | <b>Monitoring status</b><br>This message is used for the statuses of the subsystem Level Crossing which are required for display to the signaller.  |
| Eu.DK.346 | Info | <b>Failure status</b><br>This message is used when a failure occurred or is revoked.  |
| Eu.DK.436 | Info | <b>Obstacle detection status</b><br>This message is used to report an obstacle detected inside the level crossing protection area.  |
| Eu.DK.437 | Info | <b>Detection element status</b><br>This message is used to report the occupancy status of detection elements.   |
| Eu.DK.296 | Head | <b>6.5.3 Activation and deactivation of the level crossing protection facility</b>  |
| Eu.DK.297 | Info | When the level crossing protection facility is activated, it will start a sequence of warning devices and barrier movement to protect the level crossing protection area. Once this sequence has been completed, the level crossing is considered to be in the state 'protected'. |
| Eu.DK.298 | Info | When the level crossing protection facility is deactivated, a deactivation sequence will start to remove the protection of the level crossing protection area. As soon as this sequence starts, the level crossing is considered to be in the state 'unprotected'.                |
| Eu.DK.515 | Head | <b>6.6 Single-element and multi-element controllers</b>   |
| Eu.DK.516 | Info | The EULYNX field element subsystems (EfeS) can be implemented with different types of controllers.  |
| Eu.DK.517 | Info | A <b>single-element controller</b> implements one EfeS in a single physical device.   |
| Eu.DK.518 | Info | A <b>single type multi-element controller</b> implements more than one EfeS in a single physical device. All EfeS are of the same type (Light Signal, Point, Train Detection System, Generic IO or Level Crossing).   |
| Eu.DK.519 | Info | A <b>multi type multi-element controller</b> implements more than one EfeS in a single physical device. The implemented EfeS are of different types.  |
| Eu.DK.520 | Head | <b>6.6.1 Levels and multiplicities</b>  |
| Eu.DK.521 | Info | The multiplicity between one physical device and multiple controlled track elements is handled on different logical levels.   |

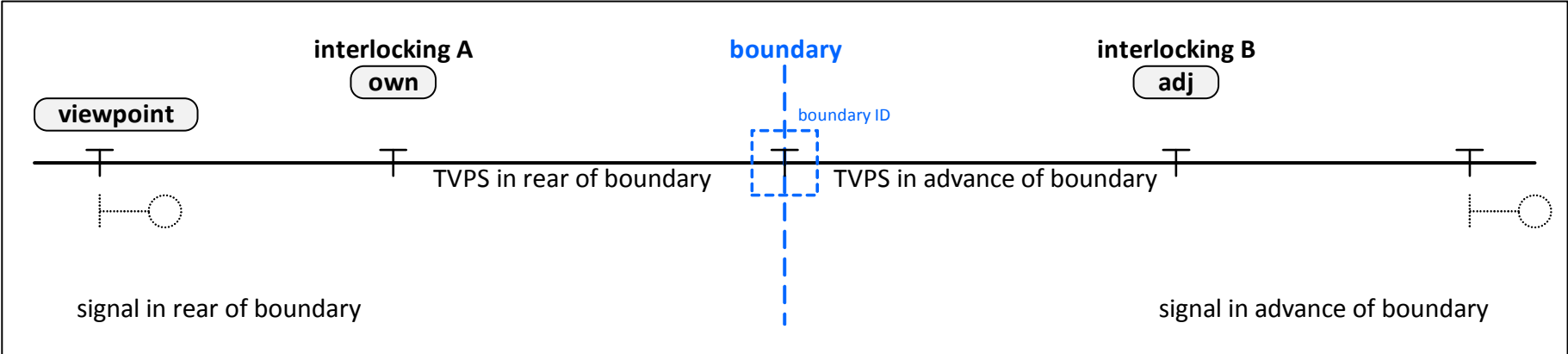
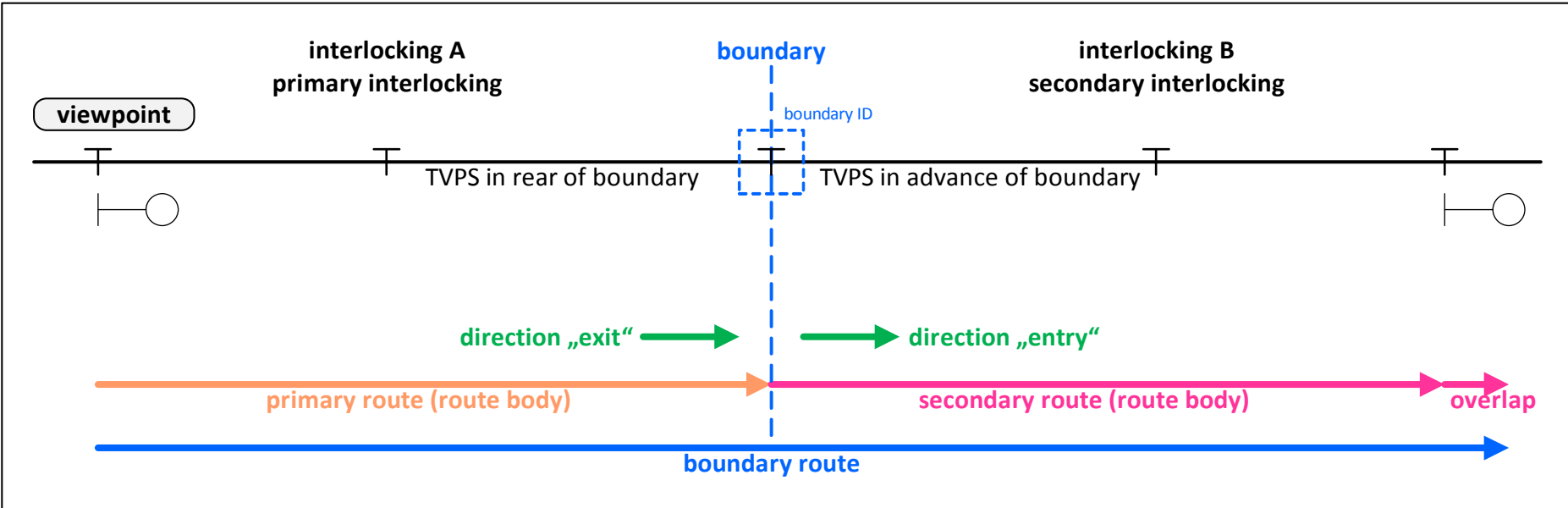
| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.551 | Info | <p>The logical levels and their multiplicities are visualised in the diagram below and described in the sections that follow.</p> <p><b>Communicaton endpoints, levels and cardinalities</b></p> <p>Legend:</p> <ul style="list-style-type: none"> <li>— RaSTA Telegrams —</li> <li>— PDI Mgmt Cmds —</li> <li>— Element specific DX —</li> </ul> <p>Levels:</p> <ul style="list-style-type: none"> <li>SCP connection: RaSTA ID</li> <li>EULYNX field element subsystem: SubS_ID (Technical ID)</li> <li>Operational element: Operational ID</li> </ul> <p>Cardinalities:</p> <ul style="list-style-type: none"> <li>1:n between SCP connection and EULYNX field element subsystem.</li> <li>1:1 between EULYNX field element subsystem and Operational element.</li> </ul> <p>Categories:</p> <ul style="list-style-type: none"> <li>P, LS, LC</li> <li>TDS, Generic IO</li> </ul> <p>Multiplicity</p> |
| Eu.DK.522 | Head | <b>6.6.1.1 Communication levels and endpoints</b>  |
| Eu.DK.523 | Info | <p><u>Operational elements</u><br/>                 The lowest logical level of the communication between the electronic interlocking and the EfeS addresses a single operational element. This is a specific light signal, point, track vacancy proving section, train detection point, adjacent IO system or level crossing.<br/>                 The telegrams of the Process Data Interface protocol include the operational identifier as Sender or Receiver Identifier when addressing a concrete operational element.</p>   |
| Eu.DK.524 | Info | <p><u>EULYNX field element subsystem</u><br/>                 A part of the communication between the electronic interlocking and the EfeS addresses the EfeS itself. This is the case for all generic PDI telegrams that are exchanged during the establishing and closing of the PDI connection.<br/>                 These generic telegrams of the Process Data Interface protocol contain the technical identifier of the EfeS as Sender or Receiver Identifier.</p>  |
| Eu.DK.525 | Info | <p><u>Safe Communication Protocol RaSTA</u><br/>                 The communication layer below the PDI protocol is the Safe Communication Protocol. The endpoints of this communication are formed by the RaSTA endpoints. For multi-element controllers, the RaSTA endpoint of the SCP connection may be located on a common system part for all the instances of EULYNX field element subsystems implemented on the device.</p>  |
| Eu.DK.526 | Head | <b>6.6.1.2 Multiplicities</b>  |
| Eu.DK.527 | Info | <p><u>Number of operational elements per EULYNX field element subsystem</u><br/>                 The EfeS for Light Signal, Point and Level Crossing control only one single operational element.<br/>                 The EfeS for Train Detection System and Generic IO control multiple operational elements (TVP sections, train detection points, logical input and output channels). A physical device implementing <i>one</i> subsystem TDS or Generic IO is considered a <i>single-element controller</i>, even if it controls multiple operational elements!</p>  |

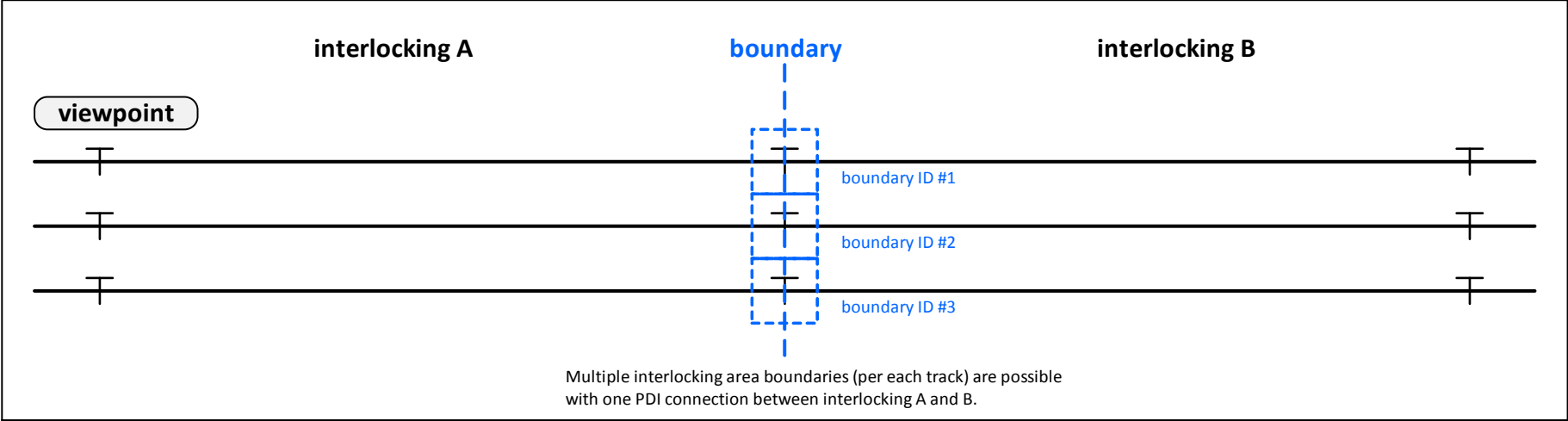
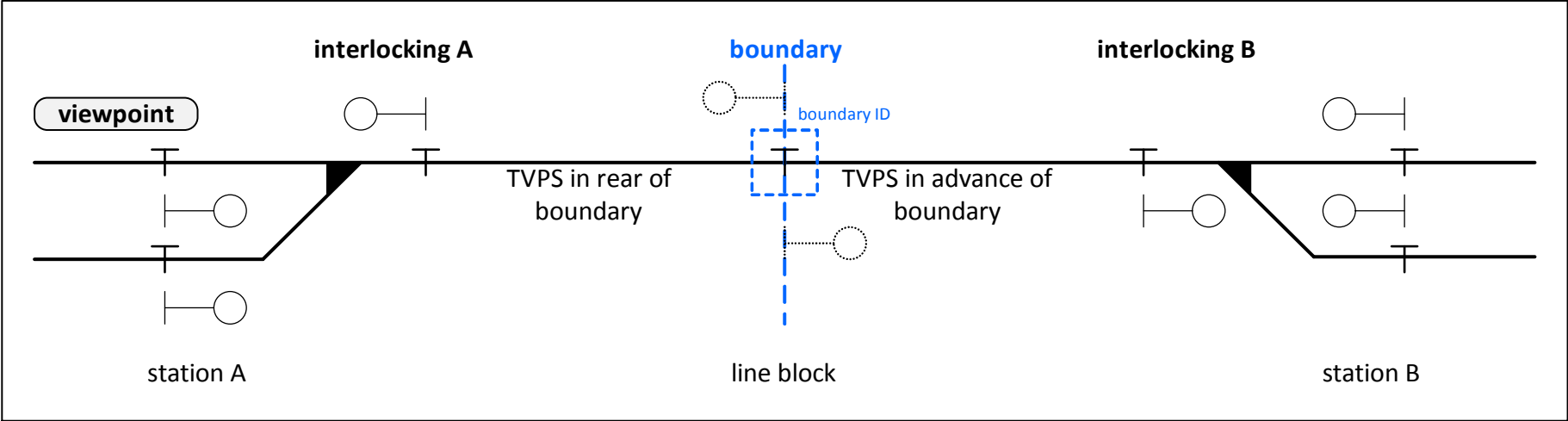
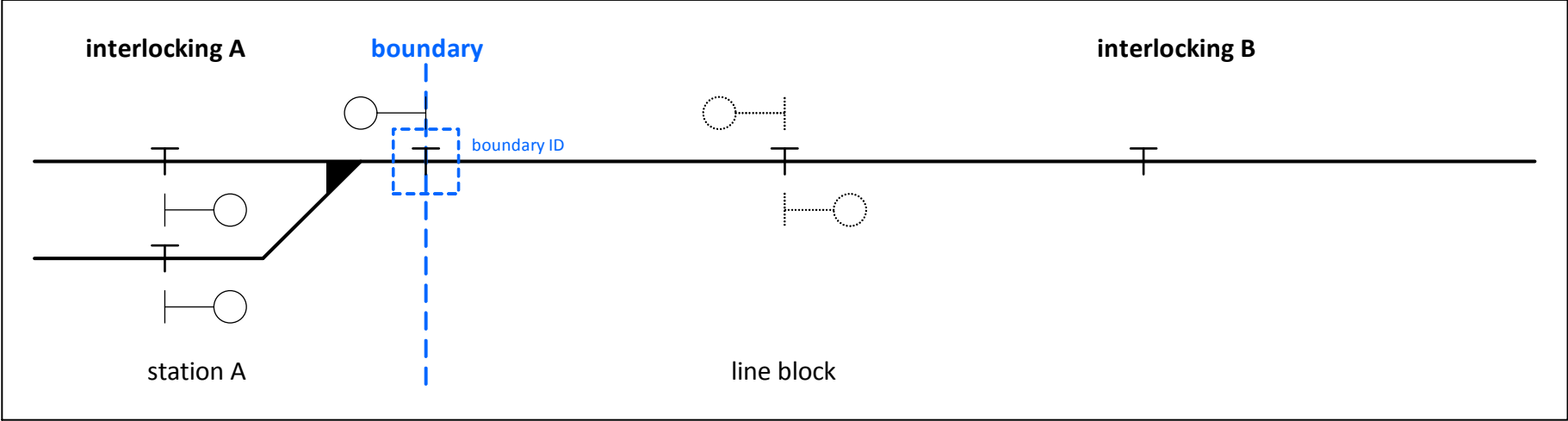
| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.528 | Info | <u>Number of PDI connections per EULYNX field element subsystem</u><br>There is always exactly one PDI connection that connects the EULYNX field element subsystem with the interlocking system.  |
| Eu.DK.529 | Info | <u>Number of PDI connections per SCP connection</u><br>A single-element controller implements one EULYNX field element subsystem and therefore has only one PDI connection, which will be stacked on one SCP connection. The multiple PDI connections of a multi-element controller (single type or multi type) can be stacked together on one SCP connection.  |
| Eu.DK.530 | Info | Because MEC platforms can share one SCP (RaSTA) channel for multiple PDI connections, the heartbeat communication related to one EfeS instance is reduced. The bandwidth requirement per EfeS instance decreases accordingly.   |
| Eu.DK.531 | Info | <u>No. of SCP connections per physical device</u><br>A single-element controller will have only one SCP connection.<br>A multi-element controller (single type or multi type) may have one or more SCP connections. Although possible, it may not be optimal to have a high number of PDI connections all stacked on a single SCP connection.   |
| Eu.DK.532 | Head | <b>6.6.2 Essential states</b>   |
| Eu.DK.533 | Info | For a concrete EfeS instance on a multi-element controller, the essential states must be regarded as abstract states, even if their naming implies a relation to hardware behaviour. By its nature, the status of the platform that implement a multi-element controller may impact the status of all the EfeS instances that it implements. This means that there are some dependencies between the EfeS states and the state of the multi-element controller.   |
| Eu.DK.534 | Info | The state can be BOOTING either because the underlying platform controller is booting or because the interface to a specific (set of) operational element(s) is booting. The state NO_POWER can be interpreted as meaning that the core functionality of an EfeS instance is turned off. The state INITIALISING of an EfeS has a fully identical meaning for multi-element and single-element controllers. The specific EfeS is ready to establish connection to the interlocking or ready to perform maintenance interaction with the MDM. |
| Eu.DK.535 | Head | <b>6.6.3 Management of SCP connection</b>   |
| Eu.DK.536 | Info | The management of the SCP connection is fully decoupled from the essential state of the EfeS. This is necessary, because on a multi-element controller it is possible that the SCP connection is provided by a different part of the controller (either physically or logically separated).   |
| Eu.DK.537 | Info | On a multi-element controller, depending on the architecture, it may be possible to have an established SCP connection while one or more of the EfeS is (re-)booting.<br>Given by its architecture, a single-element controller may have constraints to this flexibility. For a single-element controller, it will most likely not be possible to establish the SCP connection before the EfeS has finished booting and is in state INITIALISING.   |
| Eu.DK.538 | Head | <b>6.6.4 Scope of model-based specifications</b>  |
| Eu.DK.539 | Info | The model-based specifications of EULYNX don't cover the dependencies between the state of the multi-element controller and the essential states of the implemented EfeS. The management of the SCP connection is also not in the scope of the model-based specifications.  |

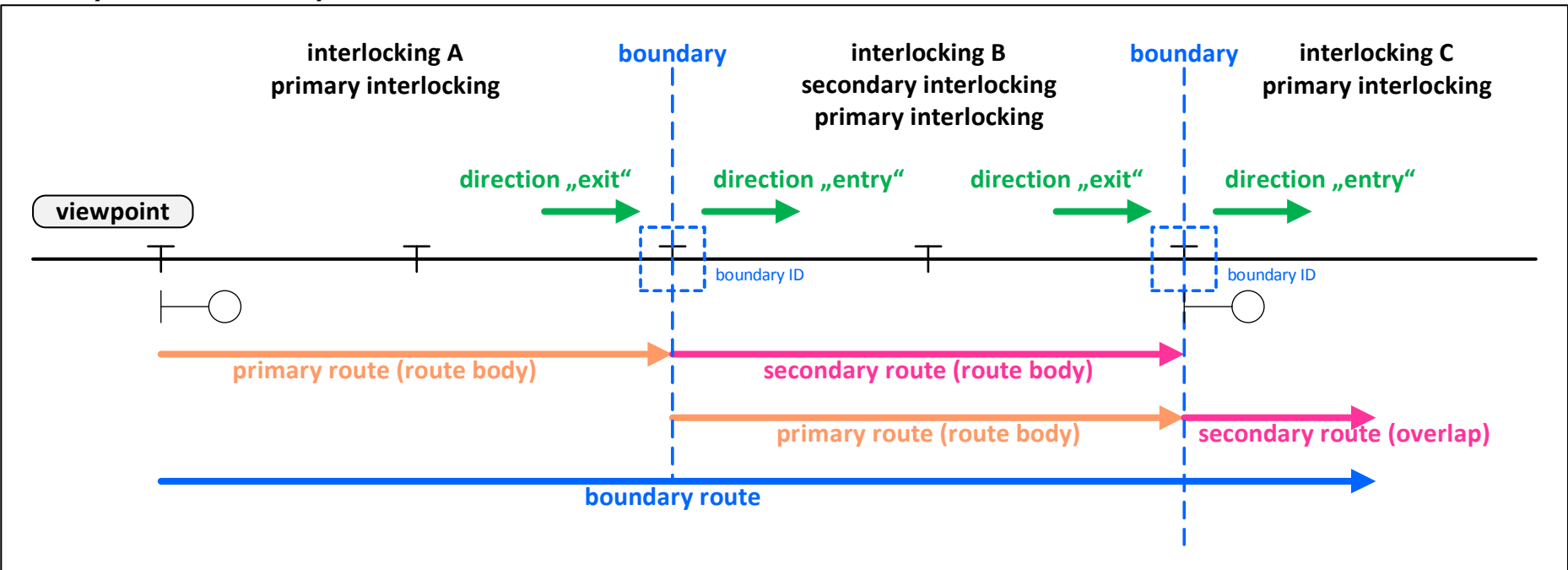
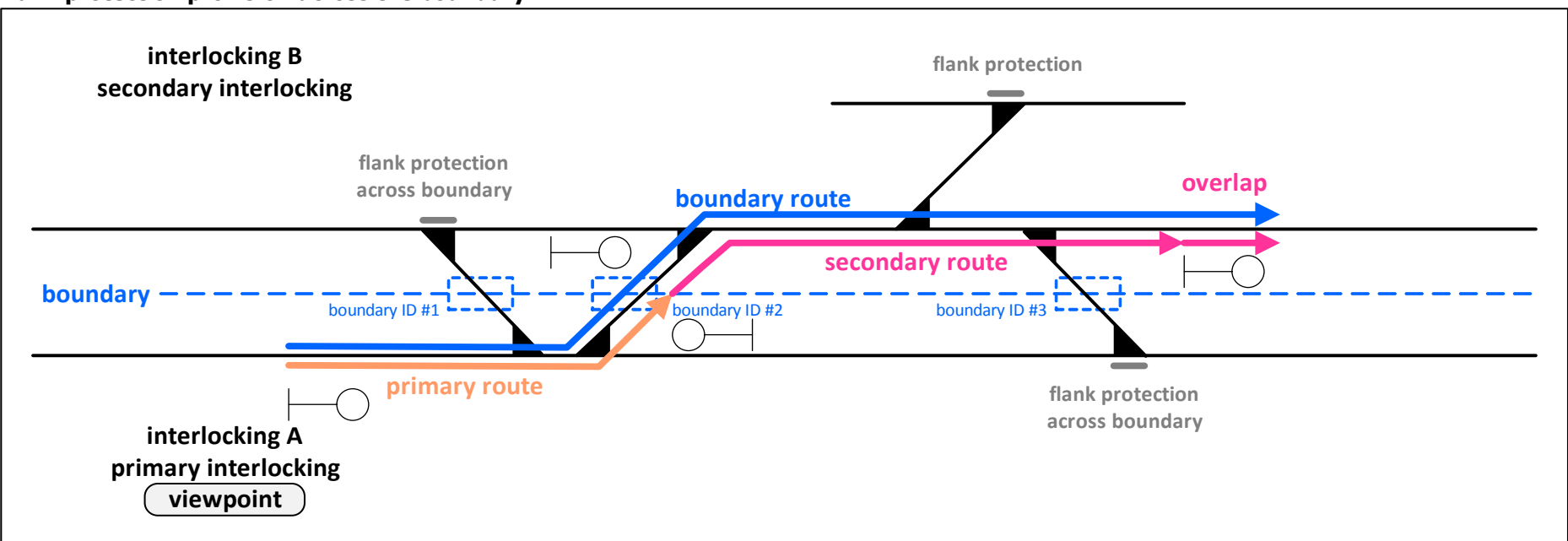
| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.552 | Info | <p>Modelling scope related to single- and multi-element controllers</p>  <p><b>Electronic Interlocking</b></p> <ul style="list-style-type: none"> <li>SCI - PRIMARY (Modelled)</li> <li>SCP (Not modelled)</li> </ul> <p><b>Single Element Controller</b></p> <ul style="list-style-type: none"> <li>SCI - SECONDARY (Modelled)</li> <li>TA Interface (Modelled)</li> <li>SCP (Not modelled)</li> <li>EST EfeS (Modelled)</li> </ul> <p><b>Multi Element Controller</b></p> <ul style="list-style-type: none"> <li>SCI - PRIMARY (Modelled)</li> <li>SCI - SECONDARY (Modelled)</li> <li>TA Interface (Modelled)</li> <li>EST EfeS Instance (Modelled)</li> <li>SCP (Not modelled)</li> <li>Essential States Multi Element Controller (Not modelled)</li> </ul> <p>Various independent EfeS instances share the same SCP</p> <p>Platform internal dependencies</p> <p><b>Legend:</b></p> <ul style="list-style-type: none"> <li>EULYNX Field Element Subsystem (Red box)</li> <li>Essential State EST EfeS (Blue box)</li> <li>Modelled (Yellow box with 'M')</li> <li>Not modelled (Yellow box with 'X')</li> </ul> |
| Eu.DK.540 | Head | <b>6.6.5 Handling of communication inside interlocking</b>  |
| Eu.DK.541 | Info | The multi-element controller is transparent for communication with the interlocking. It does not form an explicit endpoint from the point of view of the interlocking. All endpoints required by the interlocking refer to the operational element to be controlled (during operation) or to the EfeS (during initialisation).  |
| Eu.DK.542 | Info | The multiplicity among communication levels may vary. This is implicitly manifested by the addressing configuration. Each operational element that occurs in the configuration and engineering data of the interlocking must be able to be controlled via the following assignment (addressing configuration): Object X -> Operational Identifier -> Technical Identifier (EfeS) -> RaSTA ID -> IP address and/or TLS Endpoint ID.  |
| Eu.DK.543 | Info | Functionally, there is no difference in commanding an EfeS hosted on a single-element controller or a multi-element controller. The different endpoints are assigned in the configuration and engineering data of the interlocking and thus the communication paths are always resolved in the same manner.   |

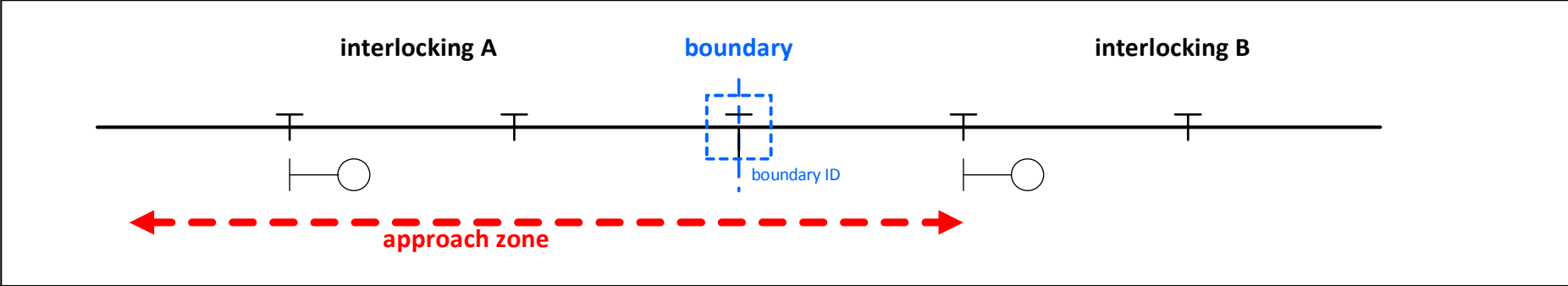
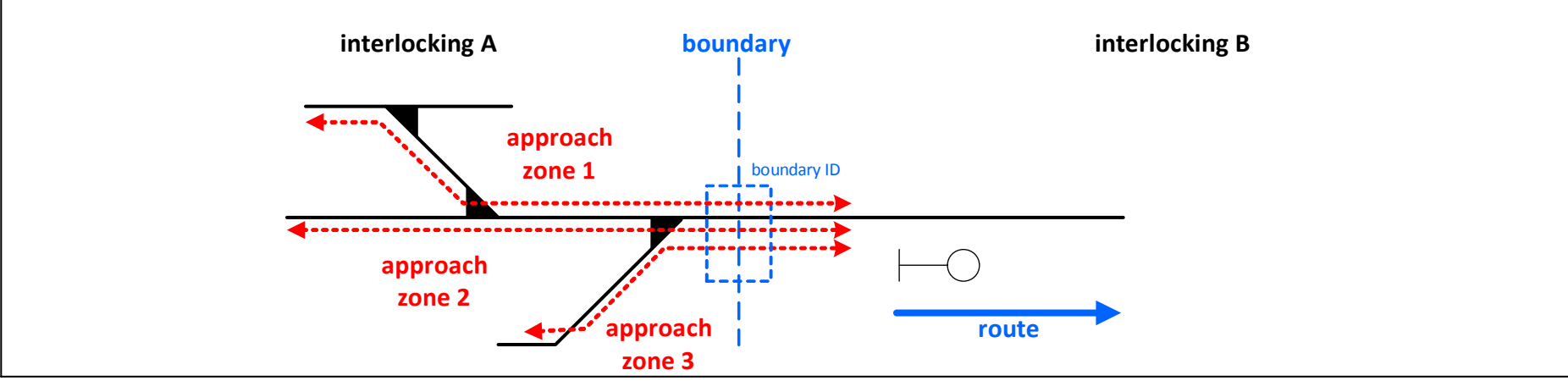
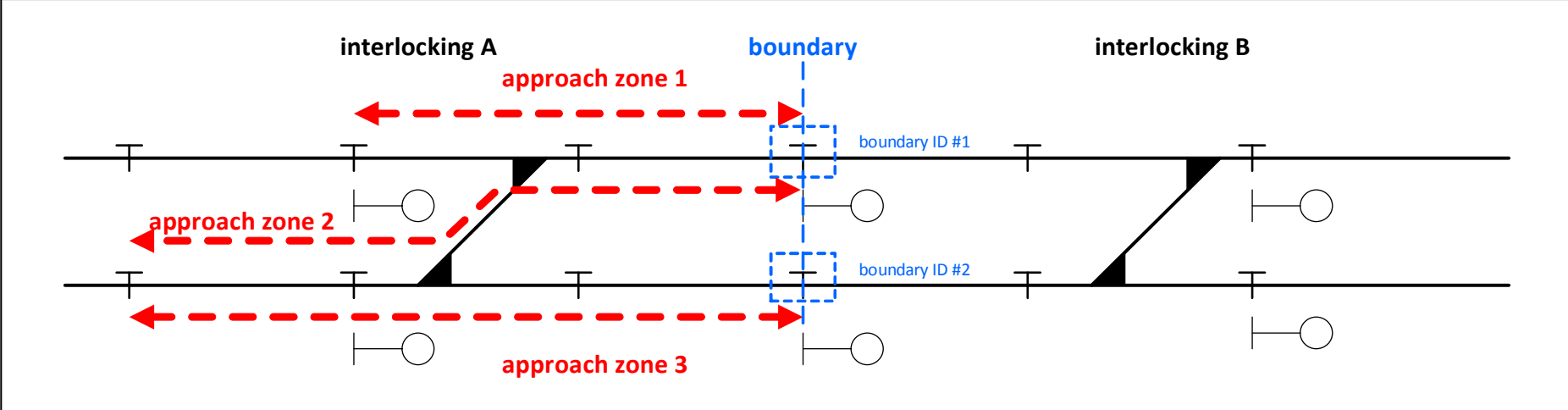
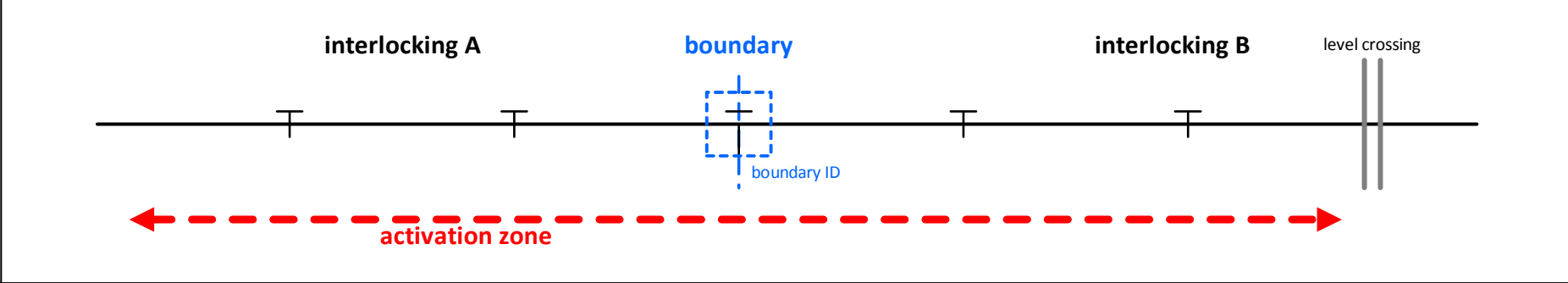


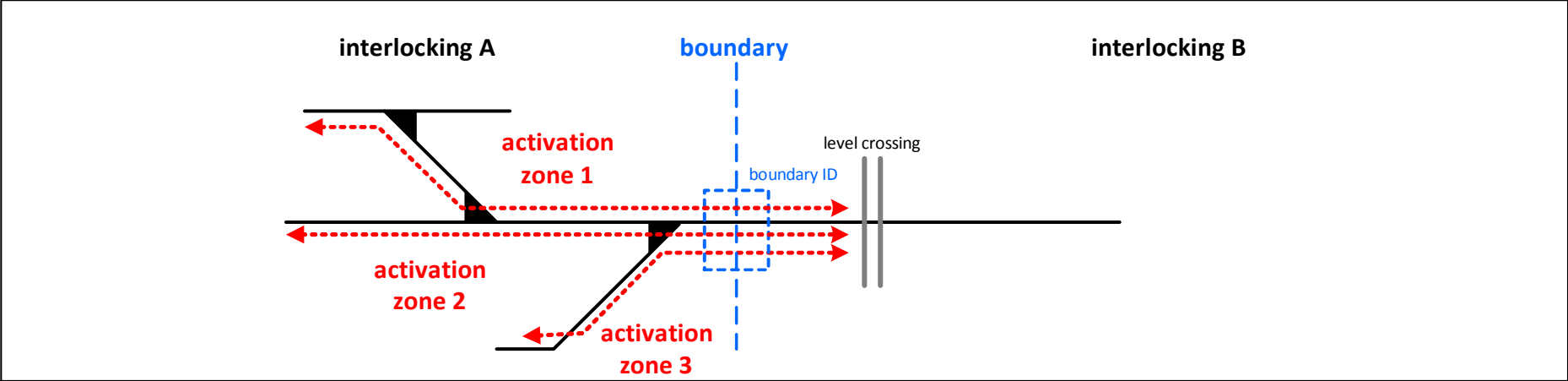
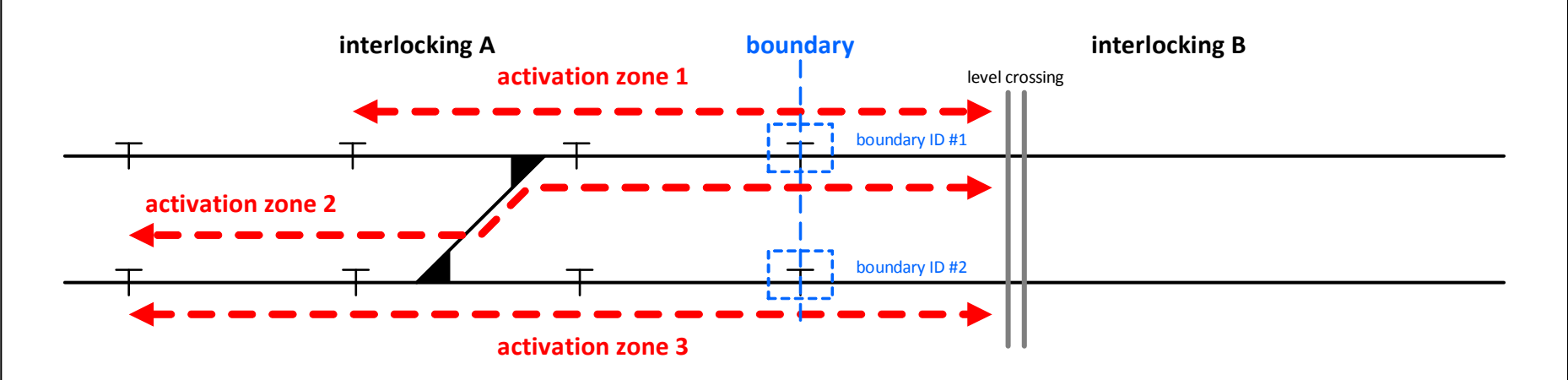
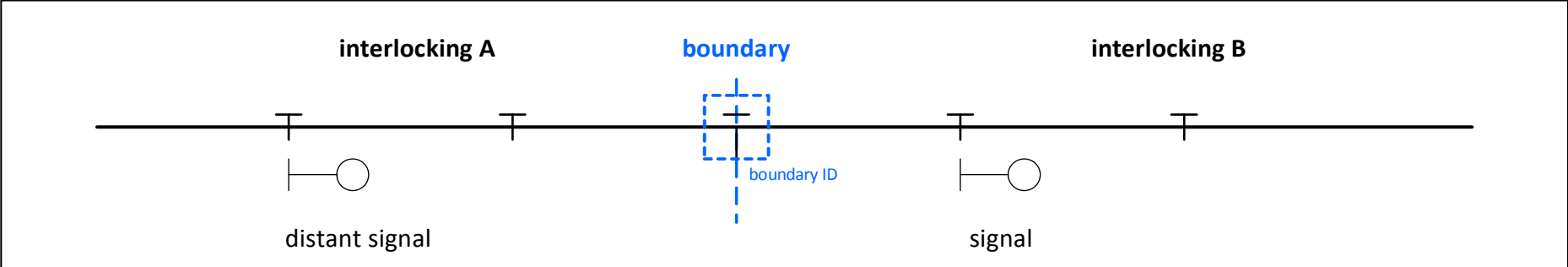
| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.553 | Info | <p>Endpoint handling</p>  <p>The diagram illustrates the endpoint handling process across four main components: EIL (Electronic Interlocking), IP Network, OC Platform (Object Controller), and Copper cable.          <ul style="list-style-type: none"> <li><b>EIL:</b> Contains an 'Object ###' and an 'Object configuration' block. The configuration includes fields for Object_ID, SubS_ID, RaSTA_ID, and IP Address and/or TLS Endpoint ID. A legend indicates that red dots represent 'Bindings of Object configuration'.</li> <li><b>IP Network:</b> Acts as the communication medium between EIL and the OC Platform. It shows connections for PDI Sender / Receiver ID, Object addressing, PDI connection, RaSTA endpoints, Safety Protocol (RaSTA), and Transport endpoints (UDP / TCP - TLS).</li> <li><b>OC Platform:</b> Contains an 'Element Configuration' block with fields for Object_ID, SubS_ID, RaSTA_ID, and IP Address and/or TLS Endpoint ID. It is connected to the IP Network via the same protocols as the EIL.</li> <li><b>Copper cable:</b> Shows an 'Object ###' connected to the OC Platform, representing the physical interface to the field element.</li> </ul> </p> |
| Eu.DK.544 | Head | <b>6.6.6 Handling of diagnostics, maintenance and security interfaces on multi-element controllers</b>  |
| Eu.DK.545 | Head | <b>6.6.6.1 Diagnostics interface</b>  |
| Eu.DK.546 | Info | Diagnostic data is sent per EfeS instance in the same way as for a single-element controllers. There is no grouping of diagnostic data for different EfeS instances.  |
| Eu.DK.547 | Head | <b>6.6.6.2 Maintenance interface</b>  |
| Eu.DK.548 | Info | EULYNX does not define how the cardinality between EfeS instances and OPC UA endpoints for SMI should be implemented. It is possible that one OPC UA endpoint can serve as a maintenance gateway for multiple EfeS instances. Therefore, the generic SMI data model supports the addressing of a specific subsystem within a MEC by a top-level node with the SubS_ID as identifier.  |
| Eu.DK.549 | Head | <b>6.6.6.3 Security interface</b>   |
| Eu.DK.550 | Info | The manufacturer can design the placement of the SSI endpoints and the use of the Security Services in such a way that the MEC concept is supported optimally and in accordance with the security specifications.   |
| Eu.DK.563 | Head | <b>7 Overall timing requirements</b>  |
| Eu.DK.564 | Info | Overall timing behaviour is governed by one safety requirement. This safety requirement defines the safety response time needed between the occurrence of an infrastructure related anomaly violating route monitoring conditions until setting the safety relevant outputs (for example signal aspect) to a safe state.  |
| Eu.DK.565 | Info | The assumed overall safety response time for an undisturbed EULYNX system is 1,6 seconds. This overall assumed time is derived by summing up the values below as follows:<br>Eu.DK.566 + Eu.DK.569 + Eu.DK.568 + Eu.DK.569 + Eu.DK.567.   |
| Eu.DK.566 | Info | For a EULYNX field element subsystem, the time span between detection of a status change at the control interface (e.g. status of lamps, point position, wheel sensor) and the sending of an SCI-XX message at the PoS-Signalling reporting this is assumed to not exceed 500 ms.<br>Note: The concrete timing requirements are defined in the requirements specification of the EULYNX field element subsystems and may differ from this value.  |
| Eu.DK.567 | Info | For a EULYNX field element subsystem, the time span between reception of an SCI-XX command at the PoS-Signalling and the respective reaction at the control interface (e.g. turning lamps on or off, start of point movement) is assumed to not exceed 500 ms.<br>Note: The concrete timing requirements are defined in the requirements specification of the EULYNX field element subsystems and may differ from this value.   |
| Eu.DK.568 | Info | For a Subsystem - Electronic Interlocking, the time span between reception of an SCI-XX message at the PoS-Signalling reporting a status change and the sending of an SCI-XX command at the PoS-Signalling to a EULYNX field element subsystem reflecting the changed status is assumed to not exceed 500 ms.<br>Note: The concrete timing requirements for the Subsystem - Electronic Interlocking are defined by national requirements.   |
| Eu.DK.569 | Info | The delay between the sender and the receiver at a PoS-Signalling assumed to not exceed 50 ms.<br>Note: This concrete timing requirement is defined in [Eu.Doc.100].  |

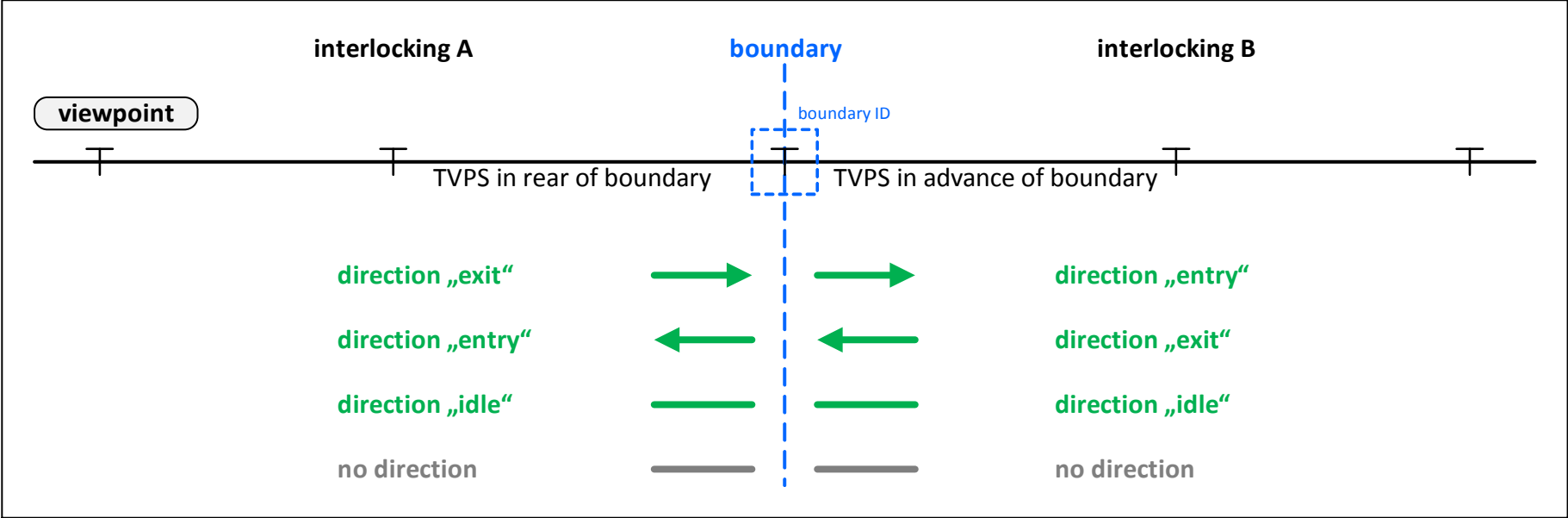
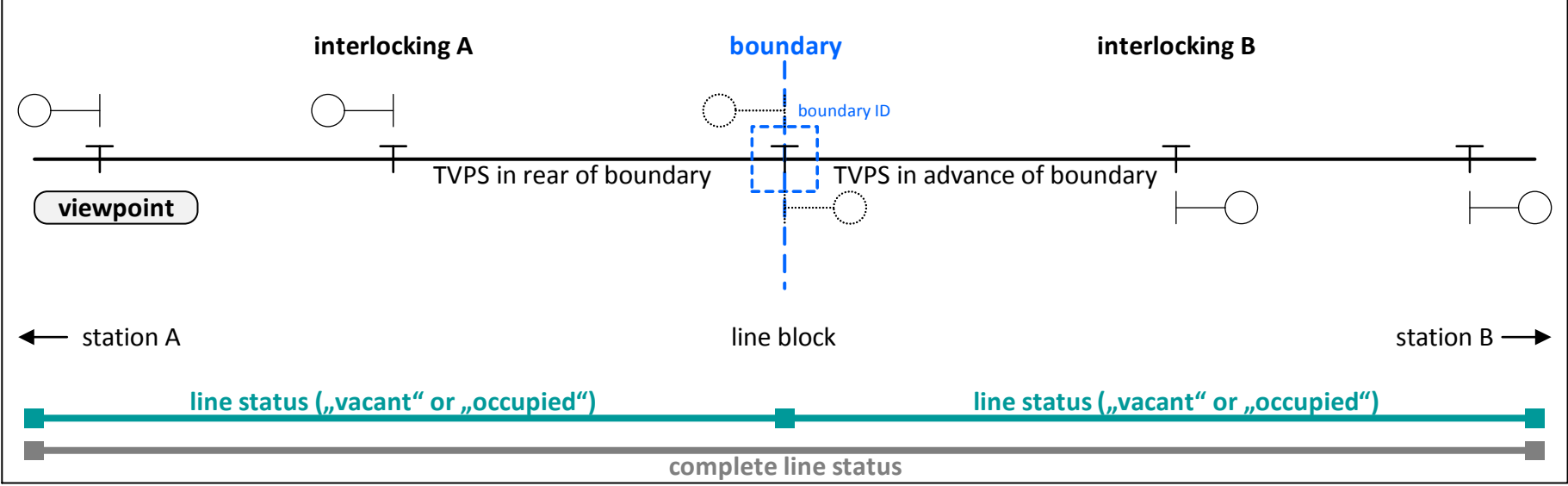
| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.571 | Info | In case a disturbance is present inside the EULYNX system, the safety response time can be higher. The most likely disturbance is related to the delay on the Subsystem – Communication System.  |
| Eu.DK.74  | Head | <b>8 Interlocking system boundaries</b>  |
| Eu.DK.75  | Info | This section describes the concept and terminology across interlocking system boundaries.  |
| Eu.DK.179 | Info | Boundaries may be located in a station area or on the open line.   |
| Eu.DK.76  | Info | The route across an interlocking system boundary is considered as a 'boundary route'.  |
| Eu.DK.77  | Info | A 'boundary route' consists of the following: <ul style="list-style-type: none"> <li>• primary route as part of the boundary route located in the primary interlocking</li> <li>• secondary route as part of the boundary route located in the secondary interlocking</li> </ul> |
| Eu.DK.78  | Info | The primary route contains the route entry signal. The secondary route contains the route exit signal. This is the default scenario.   |
| Eu.DK.79  | Info | The direction must be accounted for applications on the line.  |
| Eu.DK.80  | Head | <b>8.1 Interlocking system boundary definitions</b>  |
| Eu.DK.81  | Info | The concepts and terminology defining the use of interlocking boundaries are displayed in the following figures.   |
| Eu.DK.161 | Info | <p><b>Basic terms</b></p>   |
| Eu.DK.162 | Info | <p><b>Routes crossing the boundary between interlocking areas</b></p>    |

| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.358 | Info | <p><b>Example for multiple boundaries</b></p>  <p>Multiple interlocking area boundaries (per each track) are possible with one PDI connection between interlocking A and B.</p> |
| Eu.DK.359 | Info | <p><b>Example for line block boundary 1</b></p>    |
| Eu.DK.360 | Info | <p><b>Example for line block boundary 2</b></p>   |
| Eu.DK.86  | Head | <p><b>8.2 Boundary route across multiple boundaries</b></p>   |
| Eu.DK.87  | Info | <p>A route may be a primary route and a secondary route at the same time if multiple interlocking boundaries are passed.</p>  |

| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.164 | Info | <p><b>Boundary route across multiple boundaries</b></p>  <p>The diagram illustrates a route crossing two boundaries between three interlocking systems: interlocking A (primary), interlocking B (secondary), and interlocking C (primary). A 'viewpoint' is shown on the left. The route consists of a primary route (orange) and a secondary route (pink). A blue 'boundary route' spans across both boundaries. Green arrows indicate 'direction „exit“' and 'direction „entry“'. Labels include 'boundary ID' for each boundary and 'flank protection across boundary'.</p> |
| Eu.DK.165 | Head | <p><b>8.3 Flank protection provision across the boundary</b></p>  |
| Eu.DK.166 | Info | <p>Flank protection may be provided across a boundary by the adjacent interlocking system.</p>  |
| Eu.DK.167 | Info | <p><b>Flank protection provision across the boundary</b></p>  <p>The diagram shows a route crossing a boundary between interlocking A (primary) and interlocking B (secondary). A 'viewpoint' is located in interlocking A. The route includes a primary route (orange), a secondary route (pink), and a boundary route (blue). Three boundary IDs (boundary ID #1, #2, #3) are marked. 'flank protection across boundary' is shown as black triangles on the tracks. An 'overlap' is indicated on the secondary route.</p>  |
| Eu.DK.89  | Head | <p><b>8.4 Boundary located in the approach zone of a route</b></p>  |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.168 | Info | <p><b>Boundary located in the approach zone of a route</b></p>               |
| Eu.DK.379 | Info | <p><b>Example of multiple approach zones 1</b></p>                           |
| Eu.DK.357 | Info | <p><b>Example of multiple approach zones 2</b></p>                         |
| Eu.DK.92  | Head | <p><b>8.5 Boundary located in the activation zone of a level crossing</b></p>  |
| Eu.DK.169 | Info | <p><b>Boundary located in the activation zone of a level crossing</b></p>  |

| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.380 | Info | <p><b>Example of multiple activation zones 1</b></p>  <p>The diagram shows two interlocking areas, 'interlocking A' on the left and 'interlocking B' on the right, separated by a vertical dashed blue line labeled 'boundary'. A 'level crossing' is located in interlocking B. Three red dashed arrows represent 'activation zone 1', 'activation zone 2', and 'activation zone 3'. These zones originate from a track in interlocking A and extend across the boundary into interlocking B. A blue dashed box labeled 'boundary ID' is positioned at the boundary line.</p>                              |
| Eu.DK.381 | Info | <p><b>Example of multiple activation zones 2</b></p>  <p>The diagram shows two interlocking areas, 'interlocking A' on the left and 'interlocking B' on the right, separated by a vertical dashed blue line labeled 'boundary'. A 'level crossing' is located in interlocking B. Three red dashed arrows represent 'activation zone 1', 'activation zone 2', and 'activation zone 3'. These zones originate from tracks in interlocking A and extend across the boundary into interlocking B. Two blue dashed boxes labeled 'boundary ID #1' and 'boundary ID #2' are positioned at the boundary line.</p> |
| Eu.DK.170 | Head | <p><b>8.6 Provision of signalling information for distant signals</b></p>   |
| Eu.DK.171 | Info | <p><b>Signalling information for distant signals across a boundary</b></p>  <p>The diagram shows two interlocking areas, 'interlocking A' on the left and 'interlocking B' on the right, separated by a vertical dashed blue line labeled 'boundary'. A 'distant signal' is located in interlocking A, and a 'signal' is located in interlocking B. A blue dashed box labeled 'boundary ID' is positioned at the boundary line.</p>   |
| Eu.DK.361 | Head | <p><b>8.7 Direction</b></p>   |

| ID        | Type | Domain knowledge   |
|-----------|------|--|
| Eu.DK.365 | Info | <p><b>Definition of direction</b></p>  <p>The diagram illustrates the definition of direction between two interlocking systems, A and B, separated by a boundary. A 'viewpoint' is shown on the left side of interlocking A. The boundary is marked with a 'boundary ID'. TVPS (Train Vehicle Positioning System) locations are indicated as 'TVPS in rear of boundary' and 'TVPS in advance of boundary'. Below the track, four rows of green arrows represent different direction states: 'direction „exit“', 'direction „entry“', 'direction „idle“', and 'no direction'. The 'no direction' state is represented by a grey line.</p>     |
| Eu.DK.362 | Info | <p>Description of 'no direction': An interlocking is in the state 'no direction' regarding the line direction when the last known own direction information is not available in the interlocking during the initialisation of the PDI connection. The state 'no direction' is then sent to the adjacent interlocking during status report.</p>   |
| Eu.DK.363 | Info | <p>If both interlockings are in state 'no direction' then the direction agreement is achieved by using the pre-configured direction information which is stored in configuration data).</p>  |
| Eu.DK.364 | Info | <p>Note: The direction 'idle' is used only for a specific line block variant in which the direction is controlled by route setting and train movement. This is valid status of the direction in a operational interface regarding this line block variant. It shall not be confused with 'no direction'.</p>   |
| Eu.DK.366 | Head | <p><b>8.8 Line status</b></p>  |
| Eu.DK.367 | Info | <p><b>Line status</b></p>  <p>The diagram illustrates line status between two interlocking systems, A and B, separated by a boundary. A 'viewpoint' is shown on the left side of interlocking A. The boundary is marked with a 'boundary ID'. TVPS (Train Vehicle Positioning System) locations are indicated as 'TVPS in rear of boundary' and 'TVPS in advance of boundary'. Below the track, two rows of teal bars represent different line status states: 'line status („vacant“ or „occupied“)' and 'complete line status'. The 'complete line status' is represented by a grey bar. Arrows indicate 'station A' and 'station B'.</p> |
| Eu.DK.368 | Info | <p>The line status provides information about the status of the line between the station and the interlocking system boundary regarding one interlocking area.</p>   |
| Eu.DK.370 | Info | <p>Vacant: No train vehicle is on the line. Detailed conditions can be defined by national specifications.<br/>Occupied: A train vehicle is on the line. Detailed conditions can be defined by national specifications.</p>  |
| Eu.DK.369 | Info | <p>Line status information is exchanged between two interlockings so that each interlocking can determine the status of the whole line for further purposes in the interlocking logic.</p>   |
| Eu.DK.374 | Head | <p><b>8.9 Application variants</b></p>   |

| ID        | Type | Domain knowledge  |
|-----------|------|---|
| Eu.DK.375 | Info | <p><b>Possible application variant 1</b></p> <p>The diagram illustrates a railway track layout between two interlocking systems. On the left is 'interlocking A primary interlocking' with a 'viewpoint' and 'own' status. On the right is 'interlocking B secondary interlocking' with 'adj' status. A vertical dashed line represents the 'boundary' with a 'boundary ID'. Two TVPS (Train Positioning System) are shown: one in the rear of the boundary (under interlocking A) and one in advance of the boundary (under interlocking B). Three routes are depicted: an orange 'primary route' ending at the boundary, a pink 'secondary route' starting at the boundary, and a blue 'boundary route' passing through the boundary. A signal symbol is shown below the boundary with the note 'can also be a home signal of a station'.</p> |
| Eu.DK.377 | Info | <p><b>Possible application variant 2</b></p> <p>This diagram is similar to variant 1 but shows a different route configuration. The orange 'primary route' ends at the boundary, and the pink 'secondary route' starts at the boundary and continues past it. The blue 'boundary route' also passes through the boundary. The TVPS locations and interlocking labels remain the same.</p>   |
| Eu.DK.376 | Info | <p><b>Possible application variant 3</b></p> <p>This diagram shows a third route configuration. The orange 'primary route' starts at the boundary and continues past it. The pink 'secondary route' also starts at the boundary and continues past it. The blue 'boundary route' passes through the boundary. The TVPS locations and interlocking labels are consistent with the other variants.</p>  |